# Circuit Representation of Load and Line Asymmetries in Three-Phase Power Systems 

Diego Bellan, Gabrio Superti Furga, and Sergio A. Pignari


#### Abstract

This work deals with circuit representation of load and line asymmetries in three-phase power systems. Indeed, it is well known that under the assumption of symmetrical phases, power systems are effectively analyzed through the symmetrical component transformation leading to uncoupled sequence circuits. In practical power systems, however, the assumption of symmetrical phases is not always met for several reasons including, for example, singlephase loads (e.g., high-speed railway systems) and untransposed overhead transmission lines. From a circuit viewpoint, when the symmetrical component transformation is applied, such asymmetries result in coupled sequence circuits. As a consequence, a voltage on the negative-sequence circuit arises, leading to the well-known detrimental phenomenon of voltage unbalance emission. In this paper, a rigorous circuit representation of coupling between positiveand negative-sequence circuits is derived by means of the definition of an ideal transformer with complex turns ratio. The proposed equivalent circuit is useful to get deeper insight into the fundamental mechanisms of voltage unbalance generation and identification. Moreover, the proposed circuit representation can be readily implemented into software for circuit simulation.


Keywords-Asymmetrical three-phase loads and lines, power system analysis, symmetrical components transformation, voltage unbalance emission.

## I. Introduction

THREE-phase power systems under steady state conditions are usually analyzed in the frequency (i.e., phasor) domain by means of the symmetrical component transformation [1]. Other similar transformations are available, operating in the frequency or in the time domain [2]-[6]. The main assumption underlying such methods is the symmetry of three-phase loads and lines, i.e., the three impedances must be equal and with the same coupling coefficient (if any). The reason is clearly due to the fact that, under such assumption, the transformations mentioned above decouple the modal circuits (e.g., the positive, negative, and zero sequence circuits in the symmetrical component transformation), allowing a much easier and direct analysis. Moreover, under the assumption of system symmetry, only the positive-sequence circuit needs to be solved, whereas the negative-sequence circuit is not excited. If the assumption of symmetrical load/line is not met, the transformations provide coupled modal circuits, leading to
D. Bellan, G. Superti Furga, and S. A. Pignari are with the Department of Electronics, Information and Bioengineering, Politecnico di Milano, Milan, Italy (phone: +39-02-2399-3708; fax: +39-02-2399-3703; e-mail: diego.bellan@polimi.it).
voltages and currents also on the negative-sequence circuit, i.e., the well-known phenomenon called voltage unbalance emission [7]-[9]. In the relevant technical literature, voltage unbalance emission is analyzed by treating the coupling of positive- and negative-sequence circuits as a two-port network, but an equivalent circuit representation is not provided. Such a circuit representation, however, would be useful to get deeper insight into the phenomenon of voltage unbalance emission allowing a clear identification and characterization of each emission source [10]-[11]. This is an important issue because asymmetry can be a feature of modern power systems for several reasons. First, single-phase loads drawing significant electrical power (e.g., high-speed railway systems) result in asymmetrical loads for a three-phase system. Second, asymmetrical geometric arrangement (e.g., non-ideal transposition) of overhead transmission lines can result in asymmetrical behavior of lines.

The paper is organized as follows. In Section II the background concerning the symmetrical component transformation is recalled. In Section III, the effects of asymmetrical loads are investigated and a circuit representation is derived by resorting to the definition of an ideal transformer with complex turns ratio, whose definition and properties are presented in the Appendix. In Section IV the methodology is applied to asymmetrical transmission lines, i.e., lines characterized by conductors with unequal mutual inductances due to asymmetrical geometric arrangement. Finally, conclusions are drawn in Section V.

## II. BACKGROUND

Three-phase power systems with symmetrical loads can be conveniently analyzed by resorting to the well-known symmetrical components transformation. Indeed, even in the case of mutual coupling between the phases, the assumption of symmetrical system results in three uncoupled sequence circuits. Solving each sequence circuit is much simpler than solving the system as a whole.

The transformation matrix, in its rational form, is defined as

$$
\mathbf{S}=\frac{1}{\sqrt{3}}\left[\begin{array}{ccc}
1 & \alpha & \alpha^{2}  \tag{1}\\
1 & \alpha^{2} & \alpha \\
1 & 1 & 1
\end{array}\right]
$$

where

$$
\begin{equation*}
\alpha=e^{j \frac{2}{3} \pi}=-\frac{1}{2}+j \frac{\sqrt{3}}{2} \tag{2}
\end{equation*}
$$

and $\alpha^{2}=\alpha^{*}$. The transformation matrix is a Hermitian matrix, i.e., $\mathbf{S}^{-1}=\mathbf{S}^{T *}$.

The symmetrical components transformation when applied to phasor voltages provides

$$
\left[\begin{array}{c}
V_{+}  \tag{3}\\
V_{-} \\
V_{\mathbf{0}}
\end{array}\right]=\mathbf{S} \cdot\left[\begin{array}{c}
V_{\mathrm{a}} \\
V_{\mathrm{b}} \\
V_{\mathrm{c}}
\end{array}\right]
$$

where $V_{+}, V_{-}$, and $V_{0}$ are the positive, negative, and zero sequence voltages. Of course, the same transformation applies to phasor currents.

Symmetrical three-phase loads can be described in terms of an impedance matrix with the following structure

$$
\mathbf{Z}=\left[\begin{array}{ccc}
Z & Z_{m} & Z_{m}  \tag{4}\\
Z_{m} & Z & Z_{m} \\
Z_{m} & Z_{m} & Z
\end{array}\right]
$$

By defining the column vectors

$$
\mathbf{V}_{s}=\left[\begin{array}{l}
V_{+}  \tag{5}\\
V_{-} \\
V_{0}
\end{array}\right], \mathbf{I}_{s}=\left[\begin{array}{l}
I_{+} \\
I_{-} \\
I_{0}
\end{array}\right], \mathbf{V}=\left[\begin{array}{c}
V_{a} \\
V_{b} \\
V_{c}
\end{array}\right], \mathbf{I}=\left[\begin{array}{c}
I_{a} \\
I_{b} \\
I_{c}
\end{array}\right]
$$

the transformed current/voltage relationship for a symmetrical load can be written

$$
\begin{equation*}
\mathbf{V}_{s}=\mathbf{S Z S}^{-1} \mathbf{I}_{s}=\mathbf{Z}_{s} \mathbf{I}_{s} \tag{6}
\end{equation*}
$$

where

$$
\mathbf{Z}_{s}=\left[\begin{array}{ccc}
Z_{+} & 0 & 0  \tag{7}\\
0 & Z_{-} & 0 \\
0 & 0 & Z_{0}
\end{array}\right]
$$

and

$$
\begin{gather*}
Z_{+}=Z_{-}=Z-Z_{m}  \tag{8a}\\
Z_{0}=Z+2 Z_{m} \tag{8b}
\end{gather*}
$$

The diagonal form of the sequence impedance matrix (7) leads to the above-mentioned uncoupled sequence circuits when the transformation is applied to the whole three-phase system.

## III. Asymmetrical Loads

Let us consider an asymmetrical three-phase load consisting in three uncoupled impedances $Z_{a}, Z_{b}$, and $Z_{c}$ taking values
possibly different with respect to the average value $Z$. The assumption of uncoupled impedances is reasonable since load asymmetry results from different single-phase loads applied to each phase of the power system. We assume therefore

$$
\begin{align*}
Z_{a} & =Z+\delta Z_{a}  \tag{9a}\\
Z_{b} & =Z+\delta Z_{b}  \tag{9b}\\
Z_{c} & =Z+\delta Z_{c} \tag{9c}
\end{align*}
$$

where the impedance deviations $\delta Z_{a}, \delta Z_{b}$, and $\delta Z_{c}$ are such that $\delta Z_{a}+\delta Z_{b}+\delta Z_{c}=0$ since $Z$ is the impedance average value.

The asymmetrical impedance matrix can be written

$$
\begin{align*}
\tilde{\mathbf{z}}= & {\left[\begin{array}{ccc}
Z+\delta Z_{a} & 0 & 0 \\
0 & Z+\delta Z_{b} & 0 \\
0 & 0 & Z+\delta Z_{c}
\end{array}\right]=} \\
& =\left[\begin{array}{lll}
Z & 0 & 0 \\
0 & Z & 0 \\
0 & 0 & Z
\end{array}\right]+\delta Z_{a}\left[\begin{array}{lll}
1 & 0 & 0 \\
0 & 0 & 0 \\
0 & 0 & 0
\end{array}\right]+ \\
& +\delta Z_{b}\left[\begin{array}{lll}
0 & 0 & 0 \\
0 & 1 & 0 \\
0 & 0 & 0
\end{array}\right]+\delta Z_{c}\left[\begin{array}{lll}
0 & 0 & 0 \\
0 & 0 & 0 \\
0 & 0 & 1
\end{array}\right]= \\
& =\mathbf{Z}+\Delta_{a}+\mathbf{\Delta}_{b}+\mathbf{\Delta}_{c} \tag{10}
\end{align*}
$$

By applying the transformation (6) to (10), the average impedance matrix $\mathbf{Z}$ remains unchanged (i.e., $\mathbf{Z}_{s}=\mathbf{Z}$ ) since its structure is the same as (4) with out-of-diagonal elements equal to zero. Therefore, only the transformed version of matrices $\boldsymbol{\Delta}_{a}, \boldsymbol{\Delta}_{b}$, and $\boldsymbol{\Delta}_{c}$ must be analyzed. After some algebra, the following expressions can be obtained:

$$
\begin{align*}
& \boldsymbol{\Delta}_{s a}=\mathbf{S} \boldsymbol{\Delta}_{a} \mathbf{S}^{-1}=\frac{\delta z_{a}}{3}\left[\begin{array}{lll}
1 & 1 & 1 \\
1 & 1 & 1 \\
1 & 1 & 1
\end{array}\right]  \tag{11}\\
& \boldsymbol{\Delta}_{s b}=\mathbf{S} \boldsymbol{\Delta}_{b} \mathbf{S}^{-1}=\frac{\delta z_{b}}{3}\left[\begin{array}{ccc}
1 & \alpha^{2} & \alpha \\
\alpha & 1 & \alpha^{2} \\
\alpha^{2} & \alpha & 1
\end{array}\right]  \tag{12}\\
& \boldsymbol{\Delta}_{s c}=\mathbf{S} \boldsymbol{\Delta}_{c} \mathbf{S}^{-1}=\frac{\delta z_{c}}{3}\left[\begin{array}{ccc}
1 & \alpha & \alpha^{2} \\
\alpha^{2} & 1 & \alpha \\
\alpha & \alpha^{2} & 1
\end{array}\right] \tag{13}
\end{align*}
$$

In this work, the analysis is performed for power systems with three wires. Therefore, the zero-sequence circuit corresponding to the zero-sequence voltage and current in (6) is not defined. Thus, by taking into account (11)-(13), the positive- and negative-sequence components of the sequence impedance matrix $\tilde{\mathbf{Z}}_{s}$ provide the following relationship between positive- and negative-sequence variables:

$$
\begin{align*}
& {\left[\begin{array}{l}
V_{+} \\
V_{-}
\end{array}\right]=\left(Z\left[\begin{array}{ll}
1 & 0 \\
0 & 1
\end{array}\right]+\frac{\delta z_{a}}{3}\left[\begin{array}{ll}
1 & 1 \\
1 & 1
\end{array}\right]+\right.} \\
&  \tag{14}\\
& \left.\quad+\frac{\delta z_{b}}{3}\left[\begin{array}{cc}
1 & \alpha^{2} \\
\alpha & 1
\end{array}\right]+\frac{\delta z_{c}}{3}\left[\begin{array}{cc}
1 & \alpha \\
\alpha^{2} & 1
\end{array}\right]\right)\left[\begin{array}{l}
I_{+} \\
I_{-}
\end{array}\right]
\end{align*}
$$

From a circuit point of view, the four additive impedance terms in (14)

$$
\begin{gather*}
\mathbf{z}_{s}=Z\left[\begin{array}{ll}
1 & 0 \\
0 & 1
\end{array}\right]  \tag{15a}\\
\boldsymbol{\delta}_{s a}=\frac{\delta Z_{a}}{3}\left[\begin{array}{ll}
1 & 1 \\
1 & 1
\end{array}\right]  \tag{15b}\\
\boldsymbol{\delta}_{s b}=\frac{\delta Z_{b}}{3}\left[\begin{array}{cc}
1 & \alpha^{2} \\
\alpha & 1
\end{array}\right]  \tag{15c}\\
\boldsymbol{\delta}_{s c}=\frac{\delta Z_{c}}{3}\left[\begin{array}{cc}
1 & \alpha \\
\alpha^{2} & 1
\end{array}\right] \tag{15d}
\end{gather*}
$$

correspond to a series connection of four two-port networks (see Fig. 1) whose explicit circuit representations can be derived as follows.

The two-port network corresponding to (15a) is a pair of uncoupled impedances $Z$.

The circuit representation of (15b) consists in an impedance $\delta Z_{a} / 3$ connected in parallel with the two ports (see Fig. 2). An ideal transformer with unit turns ratio can be added for homogeneity with the following equivalent networks.

The two-port networks (15c) and (15d) are similar each other. Notice that since the two matrices in (15c) and (15d) are not symmetrical, the equivalent circuit cannot be given in terms of reciprocal components only. A simple representation, however, can be derived by resorting to the definition of an ideal transformer with complex turns ratio (see Appendix).

In fact, for the two-port network (15c) it can be easily shown that the equivalent circuit (see Fig. 3) consists in an ideal transformer with complex turns ratio given by $\alpha^{2}=\alpha^{*}$, and an impedance $\delta Z_{b} / 3$ connected in parallel to one of the two ports (in fact, by reporting the impedance through the ideal transformer its value does not change since the scaling coefficient is $\left|\alpha^{2}\right|=1$ ).

For the two-port network (15d) the circuit representation is similar to (15c), where the ideal transformer has complex turns ratio $\alpha$ instead of $\alpha^{2}$, and the parallel impedance is $\delta Z_{c} / 3$ (see Fig. 4).

The complete circuit representation of coupling between positive- and negative-sequence circuits is shown in Fig. 5.

## A. Special Case: Asymmetrical Reactive Load

The general results derived above, of course, hold also in the special case where asymmetry is only in the reactive part of the load impedances. In this case, however, a simpler equivalent circuit can be derived by means of an alternative approach.


Fig. 1. Series connection of two-port networks representing nominal impedances and coupling due to impedance asymmetries.


Fig. 2. Two-port network representing asymmetry in $Z_{\mathrm{a}}$.


Fig. 3. Two-port network representing asymmetry in $Z_{b}$.


Fig. 4. Two-port network representing asymmetry in $Z_{\text {c }}$.

In this case the asymmetrical part of the load impedance matrix can be written

$$
\mathbf{Z}=\left[\begin{array}{ccc}
j X_{a} & 0 & 0  \tag{16}\\
0 & j X_{b} & 0 \\
0 & 0 & j X_{c}
\end{array}\right]
$$



Fig. 5. Complete two-port network representation of coupling between positive- and negative-sequence circuits due to asymmetries in load impedances.

By applying the symmetrical component transformation we obtain:

$$
\mathbf{Z}_{s}=j\left[\begin{array}{ccc}
X & X_{-} & X_{+}  \tag{17}\\
X_{+} & X & X_{-} \\
X_{-} & X_{+} & X
\end{array}\right]=j \mathbf{X}_{s}
$$

where X is the average reactance

$$
\begin{equation*}
X=\frac{X_{a}+X_{b}+X_{c}}{3} \tag{18}
\end{equation*}
$$

and

$$
\begin{align*}
& X_{+}=\frac{X_{a}+\alpha X_{b}+\alpha^{2} X_{c}}{3}  \tag{19}\\
& X_{-}=\frac{X_{a}+\alpha^{2} X_{b}+\alpha X_{c}}{3} \tag{20}
\end{align*}
$$

The matrix $\mathbf{X}_{s}$ can be written as the sum of its symmetric and antisymmetric parts:

$$
\mathbf{X}_{s}=\left[\begin{array}{ccc}
X & \frac{X_{-}+X_{+}}{2} & \frac{X_{-}+X_{+}}{2}  \tag{21}\\
\frac{X_{-}+X_{+}}{2} & X & \frac{X_{-}+X_{+}}{2} \\
\frac{X_{-}+X_{+}}{2} & \frac{X_{-}+X_{+}}{2} & X
\end{array}\right]+\left[\begin{array}{ccc}
0 & \frac{X_{-}-X_{+}}{2} & \frac{X_{+}-X_{-}}{2} \\
\frac{X_{+}-X_{-}}{2} & 0 & \frac{X_{-} X_{+}}{2} \\
\frac{X_{-}-X_{+}}{2} & \frac{X_{+}-X_{-}}{2} & 0
\end{array}\right]
$$

After simple algebra we obtain:

$$
\mathbf{Z}_{s}=\left[\begin{array}{ccc}
j X & j \frac{\delta X_{a}}{2} & j \frac{\delta X_{a}}{2}  \tag{22}\\
j \frac{\delta X_{a}}{2} & j X & j \frac{\delta X_{a}}{2} \\
j \frac{\delta X_{a}}{2} & j \frac{\delta X_{a}}{2} & j X
\end{array}\right]+\left[\begin{array}{ccc}
0 & \frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}} & -\frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}} \\
-\frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}} & 0 & \frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}} \\
\frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}} & -\frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}} & 0
\end{array}\right]
$$

and finally, by taking into account only positive- and negativesequence circuits:

$$
\left[\begin{array}{l}
V_{+}  \tag{23}\\
V_{-}
\end{array}\right]=\left(\left[\begin{array}{cc}
j X & j \frac{\delta X_{a}}{2} \\
j \frac{\delta X_{a}}{2} & j X
\end{array}\right]+\left[\begin{array}{cc}
0 & \frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}} \\
-\frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}} & 0
\end{array}\right]\right)\left[\begin{array}{l}
I_{+} \\
I_{-}
\end{array}\right]
$$

The circuit representation of (23) consists in the series connection of only two components. The first component is a mutual inductor whose circuit representation can be given in terms of a real ideal transformer as in Fig. 6, where the turns ratio $k$ is given by

$$
\begin{equation*}
k=\frac{\delta X_{a}}{2 X} \tag{24}
\end{equation*}
$$

and the leakage coefficient $\sigma$ is given by

$$
\begin{equation*}
\sigma=1-k^{2}=1-\frac{\delta X_{a}^{2}}{4 X^{2}} \tag{25}
\end{equation*}
$$

The second component in (23) corresponds to a gyrator with resistive parameter $r$ given by

$$
\begin{equation*}
r=\frac{\delta X_{c}-\delta X_{b}}{2 \sqrt{3}} \tag{26}
\end{equation*}
$$

The complete circuit representation of coupling due to an asymmetric reactive load is shown in Fig. 7.


Fig. 6. Circuit representation of the inductive coupling component between positive and negative sequence circuits.


Fig. 7. Complete circuit representation for the coupling between positive and negative sequence circuits due to an asymmetrical reactive load.

A significant particular case is when only positive-sequence voltage is imposed to an asymmetrical load. In this case, in a three-wire system, from (23) with $V_{-}=0$ it can be readily obtained

$$
\begin{align*}
& I_{+}=-\frac{j X}{X^{2}-\left(\frac{\delta X_{a}}{2}\right)^{2}-\left(\frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}}\right)^{2}} V_{+}  \tag{27a}\\
& I_{-}=j \frac{\frac{\delta X_{a}}{2}+j \frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}}}{X^{2}-\left(\frac{\delta X_{a}}{2}\right)^{2}-\left(\frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}}\right)^{2}} V_{+} \tag{27b}
\end{align*}
$$

Eq. (27b) shows that load asymmetry results in a negativesequence current even if a symmetrical voltage is imposed.

Moreover, from (27a)-(27b) the squared value of the total rms three-phase current can be evaluated

$$
\begin{gather*}
I_{T}^{2}=\left|I_{a}\right|^{2}+\left|I_{b}\right|^{2}+\left|I_{c}\right|^{2}=\left|I_{+}\right|^{2}+\left|I_{-}\right|^{2}= \\
=\frac{X^{2}+\left(\frac{\delta X_{a}}{2}\right)^{2}+\left(\frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}}\right)^{2}}{\left[X^{2}-\left(\frac{\delta X_{a}}{2}\right)^{2}-\left(\frac{\delta X_{b}-\delta X_{c}}{2 \sqrt{3}}\right)^{2}\right]^{2}}\left|V_{+}\right|^{2} \tag{28}
\end{gather*}
$$

From (28) it can be observed that load asymmetry results always in an increased value of the total rms three-phase current.

## IV. Asymmetrical Lines

Power systems lines are usually transposed along their length such that each of the three conductor can be considered geometrically equivalent to the others on average basis. When the length of the line does not allow transposition, however, the mutual inductance between each conductor pair cannot be considered the same for each conductor pair [7]-[8]. A typical example is given by a line with a horizontal arrangement of the three conductors. In this case, the mutual inductance between the outer conductors is lower than the two pairs including the inner conductor. This arrangement results in an asymmetric impedance matrix in the out-of-diagonal coefficients 1-3. The impedance matrix in this case can be written

$$
\widetilde{\boldsymbol{Z}}=\boldsymbol{Z}+j \delta X_{m}\left[\begin{array}{lll}
0 & 0 & 1  \tag{29}\\
0 & 0 & 0 \\
1 & 0 & 0
\end{array}\right]
$$

where Z is the symmetric part of the impedance matrix, and $\delta X_{m}$ is the deviation of the mutual reactance 1-3 due to geometric asymmetry of conductor arrangement. By applying the symmetrical component transformation to (29), and by taking only positive and negative sequence variables as in Section III, for the asymmetric part (responsible for circuit coupling) we obtain

$$
\left[\begin{array}{l}
V_{+}  \tag{30}\\
V_{-}
\end{array}\right]=-j \frac{2}{3} \delta X_{m}\left[\begin{array}{cc}
\frac{1}{2} & \alpha \\
\alpha^{2} & \frac{1}{2}
\end{array}\right]\left[\begin{array}{l}
I_{+} \\
I_{-}
\end{array}\right]
$$

whose circuit representation can be given in terms of an ideal transformer with complex turns ratio as in Fig. 8.


Fig. 8. Circuit representation of coupling due to line asymmetry.

## Appendix

An ideal transformer with complex turns ratio $k$ is defined by (passive sign convention at the two ports):

$$
\begin{gather*}
V_{1}=k V_{2}  \tag{A1}\\
I_{2}=-k^{*} I_{1} \tag{A2}
\end{gather*}
$$

Notice the complex conjugate of $k$ in (A2). Indeed, such definition leads to conservation of complex power:

$$
\begin{equation*}
S_{1}=V_{1} I_{1}^{*}=k V_{2}\left(\frac{-I_{2}}{k^{*}}\right)^{*}=-V_{2} I_{2}^{*}=-S_{2} \tag{A3}
\end{equation*}
$$

Moreover, the scaling coefficient for reporting impedances from the secondary to the primary side is $|k|^{2}$. In fact, an impedance $Z_{2}$ on the secondary side results in a primary side equivalent impedance

$$
\begin{equation*}
Z_{1}=\frac{V_{1}}{I_{1}}=\frac{k V_{2}}{-\frac{I_{2}}{k^{*}}}=k k^{*} \frac{V_{2}}{-I_{2}}=|k|^{2} Z_{2} \tag{A4}
\end{equation*}
$$

## References

[1] C. L. Fortescue, "Method of symmetrical coordinates applied to the solution of polyphase networks," Trans. AIEE, pp. 1027-1140, 1918.
[2] G. C. Paap, "Symmetrical components in the time domain and their application to power network calculations," IEEE Trans. on Power Systems, vol. 15, no. 2, pp. 522-528, May 2000.
[3] G. Chicco, P. Postolache, and C. Toader, "Analysis of three-phase systems with neutral under distorted and unbalanced conditions in the symmetrical component-based framework," IEEE Trans. on Power Delivery, vol. 22, no.1, pp. 674-683, Jan. 2007.
[4] H. Denoun, N. Benyahia, M. Zaouia, N. Benamrouche, S. Haddad, and S. Ait Mamar, "Modelling and Realisation of a Three-Level P.W.M Inverter Using a D.S.P Controller," International Journal of Circuits, Systems and Signal Processing, vol. 8, pp. 154-159, 2014.
[5] I. Ghadbaneand and M. T. Benchouia, "Feedback linearized control based three phase shunt active power filter," International Journal of Circuits, Systems and Signal Processing, vol. 7, no. 1, pp. 18-25, 2013.
[6] M. Nayeripour and M. M. Mansouri, "Symmetrical Components Definition and Analyze for Power Electronic Converters in Nonsinusoidal Conditions, " WSEAS Trans. on Power Systems, vol. 9, pp. 388-394, 2014.
[7] P. Paranavithana, S. Perera, R. Koch, and Z. Emin, "Global voltage unbalance in MV networks due to line asymmetries," IEEE Trans. on Power Delivery, pp. 2353-2360, Oct. 2009.
[8] Z. Emin and D. S. Crisford, "Negative phase-sequence voltages on E\&W transmission system," IEEE Trans. on Power Delivery, vol. 21, no. 3, pp. 1607-1612, July 2006.
[9] "Electromagnetic compatibility (EMC)—limits—Assessment of emission limits for the connection of unbalanced installations to MV, HV and EHV power systems," IEC Tech. Rep. 61000-3-13, 2008.
[10] D. Bellan, "Coupling of Three-Phase Sequence Circuits Due to Line and Load Asymmetries," in Proc. 3rd NAUN International Conference on Circuits, Systems, Communications, Computers and Applications, Florence, Italy, Nov. 22-24, 2014, pp. 127-131.
[11] D. Bellan and G. Superti Furga, "Modified Sequence Circuits for Asymmetrical Three-Phase Loads," in Proc. of $6^{\text {th }}$ IEEE Power India International Conference, New Delhi, India, Dec. 5-7, 2014, pp. 1-4.

