Decoding-Assisted Inter Prediction for HEVC

Yi-Sheng Chang and Yinyi Lin
Department of Communication Engineering
National Central University, Taiwan 32054, R.O.C.
Email: yilin@ce.ncu.edu.tw

Abstract — In this paper, we suggest a decoding-assisted inter prediction algorithm for HEVC to improve its coding performance. In the proposed algorithm, both sum of absolute bi-directional prediction differences (SABPD) and template matching (TM) operations are employed to help merge mode decision and inter mode decision in HEVC. The experimental results reveal that the proposed algorithm outperforms the inter prediction proposed in HEVC, and the results demonstrate that average 2.2% BDBR reduction can be achieved with 11% increase of encoding time, compared to the original HEVC inter prediction.

I. INTRODUCTION

The newly developed High Efficiency Video Coding (HEVC) achieves much better coding efficiency, compared to the previous coding standard H.264/AVC, due to its complicated coding techniques. For instance, HEVC extended inter/intra prediction with block size up to 64x64 for mode decision instead of 16x16 macroblock and used three hierarchical unit representations (including coding unit (CU), prediction unit (PU) and transform unit (TU)) to obtain the optimal coding efficiency based on the quad-tree structure. HEVC supports four coding units: CU0(64x64), CU1(32x32), CU2(16x16) and CU3(8x8). There are eleven candidate PU modes in each CU level of size 2Nx2N: Merge mode, eight inter modes (2Nx2N, 2NxN, Nx2N, 2NxNfrom, 2NxNto, nLx2N and nRx2N, and NxN), and two intra modes (2Nx2N and NxN). Both inter NxN and intra NxN are only available in the smallest CU (i.e., CU3).

The HEVC encoder uses five merge candidates obtained from both spatial and temporal blocks for merge mode decision. As illustrated in Fig. 1, among the five candidates, four candidates are selected from spatially adjacent blocks \( A_1, B_1, B_0 \) and \( A_0 \) (or \( B_2 \)), and one candidate is selected from temporal blocks \( T_0 \) or \( T_1 \). If the number of available spatial and temporal merge candidates is less than five, both combined merge candidates and zero-motion vector merge candidates are considered. The five merge candidates are indicated by a merge index that is entropy-coded using a simple prefix code, with associated code words displayed in Fig. 1. The best merge candidate is determined based on rate distortion optimization (RDO) evaluation.

![Fig. 1 Merge candidates used in HEVC merge mode decision](image)

The inter mode decision in HEVC uses three motion vector prediction (MVP) candidates (referred to as advanced MVP, AMVP) for motion estimation (ME). Among them two candidates are chosen from spatial adjacent blocks and one from temporal blocks that are derived from the same positions as shown in Fig. 1, but with different derivation order. The best candidate is similarly determined with RDO technique. The motion vector difference between AMVP candidate and the true MV derived from motion estimation (referred to as MVD) is encoded and transmitted together with other information, e.g., AMVP and reference frame indexes.

For inter coded blocks, a great amount of the bitrates is required for coding MVD information. To improve coding performance of inter prediction, many decoder-side motion derivation schemes have been investigated [1-5]. Some algorithms [1-3] use template matching (TM) at both encoder and decoder sides to derive MVD as well as reference picture information, to reduce encoded bitrates. Others [4-5] use sum of absolute bi-directional prediction differences (SABPD) technique for low bitrates B-frame coding or frame rate up-conversion.

In this paper we propose a decoding-assisted HEVC inter prediction algorithm that uses SABPD and/or TM criteria to both merge mode decision and inter mode
decision to improve the coding efficiency. This paper is organized as follows. The proposed merge candidate decision scheme for merge mode decision is introduced in Section 2. Section 3 describes the proposed algorithm. The experimental results are shown and discussed in Section 4 and Section 5 gives conclusion.

II. MERGE CANDIDATE DECISION SCHEME FOR MERGE MODE DECISION

HEVC uses five merge candidates obtained from both spatial and temporal blocks for merge mode decision. Although the spatiotemporal merge mode decision proposed in HEVC is shown to achieve better coding efficiency, compared to temporal direct mode decision or spatial direct mode decision respectively proposed in H.264/AVC, it requires explicit coding the merge index information. In addition, the merge mode decision has high computational load since five RDO operations have to be performed to select the best merge candidate.

In [4], Ates et al. proposed a decoder-derived motion vector estimation scheme in H.264/AVC B-frame prediction to omit explicit coding of MVD information, based on SABPD criterion. To eliminate coding of merge index information we apply the SABPD operation to select the best merge candidate instead of RDO operation. Since SABPD operation can be derived at decoder side, and as a result coding of the merger index is not required. The experimental results showed that the SABPD-based scheme achieves slightly worse coding performance. This is due to that the SABPD technique is a suboptimal one compared to RDO technique, and less than 50% of best candidates are selected based on SABPD. The experimental study also indicates that the SABPD operation is not accurate enough to select the best merge candidate, especially for occlusion areas. In the study we applied the TM operation together with SABPD to select the best candidate and the results showed that its accuracy can be improved.

Another statistical study also indicates that with the RDO technique there is a very high probability of selecting the merge index 0 as best candidate. Based on these observations in this section we suggest a merge candidate decision algorithm for merge mode decision to reduce merge index overhead.

In the proposed algorithm we first apply SABPD/TM operations to select the best merge candidate among all merge indexes except index 0 and compare the best merge candidate with merge candidate index 0 based on the RDO operation. The one with minimum RDO cost is selected. The proposed algorithm only requires a 1-bit flag added in the syntax to signal the best merge candidate, merge index 0 or one from other indexes. If the best merge candidate is from other merge indexes, the decoder then performs SABPD/TM operation at decoder side to find out the best merge candidate. The HEVC encoder uses five merge candidates obtained from both spatial and temporal blocks for merge mode decision. In the proposed algorithm we also consider other merge candidates from temporal blocks in addition to the five candidates used in HEVC. This can enhance its coding efficiency and causes no extra bitrates. Figure 2 shows other merge candidate indexes from temporal blocks in addition to $T_0$ and $T_1$, obtained based on their occurrence.

![Fig. 2 Extra merge candidate index with $N=5, 6, ..., 83$](image)

In the SABPD/TM operation the best merge candidate among indexes $\{1, 2, 3 \ldots N\}$ is selected, instead of using RDO technique, by minimizing SABPD/TM between prediction blocks

$$\text{Merge}_{\text{best}} = \arg \min_{\text{Merge}_k} [SABPD_k + TM_{b,k} + TM_{f,k}, k \in \{1, 2, 3, \ldots , N\}]$$

With

$$SABPD_k = \sum_{i,j} B_i(x, y) - B_{i+1, j}$$

$$TM_{b,k} = \sum_{x,y} B_i(x, y) - B_{i+1, j}(x + r_x, y + s_j)$$

$$TM_{f,k} = \sum_{x,y} B_i(x, y) - B_{i+1, j}(x - u, y - v)$$

The inverse L-shape template matching region is obtained by extending N/2 pixels from the top and left of the target region for 2Nx2N merge candidate decision, as depicted in Fig. 3 [2].
The proposed merge candidate decision scheme for merge mode decision is shown in Fig. 4. The BDBR (BDPSNR) performance is evaluated in the test model HM15.0 with IntelCore i7 3.4GHz and compared for various merge candidate number. The experimental results are displayed in TABLE I. As shown, the proposed scheme can save average 1% of bitrates compared to HEVC with same merge candidates. Furthermore the proposed algorithm achieves average 1.2% bit rate reduction when more candidates are considered. The increases of average encoding time are -1%, 0.3%, 2.3% and 9%. The result reveals that 1% of computation time can be saved in proposed algorithm with same merge candidates. This is because only two (instead of five) RDO operations are required. There is a competitive encoding time are -1%, 0.3%, 2.3% and 9%. The result reveals that 1% of computation time can be saved in proposed algorithm with same merge candidates. This is because only two (instead of five) RDO operations are required. There is a competitive time saving in both uni-directional prediction and bi-directional prediction motion prediction in HEVC encoder we also perform the proposed inter mode prediction, in addition to regular uni-directional ME process and the one with TM operations respectively. The same search algorithm in regular ME is used. The coded mode is selected through RDO procedure. The 1-bit flag added in the syntax for merge candidate decision can be reused to indicate the MVD in the coded mode is derived from regular ME process or from SABPD/TM process. The block diagram of the proposed HEVC inter prediction is depicted in Fig. 4.

As shown in Fig. 4, the proposed inter prediction algorithm requires extra computations for performing SABPD/TM operations. To reduce its computational complexity, in the proposed algorithm we first perform the early termination scheme have been investigated in literatures [1-3]. 

In this paper we also incorporate both SABPD and TM operations into the inter mode prediction. In the proposed inter mode prediction, in addition to regular motion prediction in HEVC encoder we also perform the both uni-directional prediction and bi-directional prediction to derive the MVD estimates based on TM and SABPD/TM operations respectively. The same search algorithm in regular ME is used. The coded mode is selected through RDO procedure. The 1-bit flag added in the syntax for merge candidate decision can be reused to indicate the MVD in the coded mode is derived from regular ME process or from SABPD/TM process. The block diagram of the proposed HEVC inter prediction is depicted in Fig. 4.

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In this section, we compare the performance of the proposed inter prediction algorithm with original HEVC encoder. We implement these algorithms into the HM encoder HM15.0 to evaluate their performance. Figures 5 and 6 compare rate-distortion performance for various QPs, tested on PeopleOnStreet and Flowervase video sequences. Three curves are shown: original HEVC, proposed algorithm with/without early termination scheme. As shown, the proposed algorithm significantly outperforms original HEVC.

To obtain further insight, the proposed algorithm was tested on several video sequences of different formats (including classes A, B, C, D and E). We compare their BDBR, BDPSNR performance as well as total encoding time and the results are demonstrated in TABLE II for QP=22, 27, 32 and 37. As shown, the proposed algorithm achieves average 2.3% and 2.2% BDBR reduction (or equivalently 0.1 dB coding gain), compared to the original HEVC inter prediction. The increases of encoding time are 16% and 11% respectively. Average 5% of computation time can be saved with the use of early termination scheme and it does not degrade too much coding performance.

V. CONCLUSION

In this paper we suggest a decoding-assisted algorithm for HEVC inter prediction to improve its coding efficiency. The proposed algorithm uses both sum of absolute bi-directional prediction differences (SABPD) and template matching (TM) operations in merge mode decision and inter mode decision in HEVC. The experimental result shows that average 2.2% of bitrates can be reduced compared to the original HEVC, with only 11% increase in encoding time.
REFERENCES


