Harmonic Mitigation In Grid-Tied TPSPPS Via Novel PRESH Controller

Uma Yadav^{1*}, Anju Gupta², Rajesh Kr Ahuja³ ^{12,3} Department of Electrical Engineering, J C Bose University of Science and Technology YMCA Faridabad-, India *er.yadayuma11@gmail.com, 19001903004@jcboseust.ac.in

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Abstract— The proposed paper deals with a new type of controller credited with a name of "Novel PRESH" helpful in mitigating the harmonic of Grid Tied TPSPPS (Grid-Tied Three Phase Solar Photovoltaic Power System) even during abnormal grid condition. Novel PRESH controller is formed by combining the characteristic of PRES and **RESH** Controller and its harmonic mitigation analysis is done with the help of both Bode-Plot and MATLAB Simulink. Presently, for eliminating the harmonic from Grid -Tied TPSPPS, various filters and controllers have already been proposed but it either resulted in increase of computational load or make the system bulky. Earlier technique develops in eliminating the harmonic face difficulty in eliminating the harmonic when the reference waveform get distorted from its original position. The proposed "Novel PRESH" controller can also be incorporated when grid suffers from any abnormal condition. The presented paper will also discuss the performance analysis of controller along with its design procedure that are helpful in mitigating the THD of three phase SPPS near to 1% and thus satisfying the IEEE standard 519 and 1547.

Keywords—GTSPPS, PRES, PRESH, RESH, THD.

I. INTRODUCTION

Solar Photovoltaic Power System (SPPS) can be connected to grid using converter [1]-[8]. The converter used here will ensure flow of power from SPPS to grid. SPPS can also be connected with other local load, energy storage [9]-[11]. The power quality of grid mainly depends upon voltage, frequency, harmonic, etc [12]-[14]. In order to have good power-factor and minimum line losses the harmonic content of grid connected solar photovoltaic system should be as low as to 1%.

Photo-Voltaic system should have low current harmonic distortion so that all other equipments connected to grid do not face any adverse effects. Acc. to IEEE standard 519 and 1547, THD (Total Harmonic Distortion) is kept below 5% [15].

Generally, for reducing current harmonic of SPPS, earlier

technique developed for eliminating the harmonics are through: lead-lag [16], modified PI [17]-[18], repetitive [19]-[20], dead-beat [21], PR controller [22]-[24], shunt filter [25]-[29], PRES Controller [30]-[35].

Common technique adopted before for eliminating harmonics among researcher was to make use of resonant current controller [22]-[23]. This controller makes use of proportional resonant and resonant compensator. The function of duo is to keep track of fundamental reference current signal and for attenuating the current harmonics. With normal condition, this controller as referred in reference [23]-[24] work perfectly, but when abnormal condition arises, this compensator performance deteriorates resulting in increase of current harmonic distortion.

The simplest technique for eliminating the current harmonic is by engaging the PRES because of its modularity. In PRES controller, K_P (Proportional gain) is added with resonant path which is set at desired frequency.

Several advanced techniques for mitigating the harmonic have been discussed in details [36]-[41]. These control techniques make use of separately generated current reference by the help of positive and negative sequence component. Phase locked loop Algorithm as discussed in [30] and [39]-[40] does not get affect from the voltage harmonic, inter-harmonic and imbalance conditions. But these control techniques are complex and have high computational loading problem. The alternative for these techniques is achieved through [42]-[44] where Resonant Harmonic Controller is incorporated in series with Proportional resonant controller instead of connecting in parallel configuration.

This paper at first step discuss the design procedure of Novel PRESH controller.

In second step, performance analysis of Novel PRESH controller will be seen. For eliminating the higher order harmonic, PRES compensator is connected in parallel with RESH controller to make a Novel PRESH controller instead of conventional series connection as discussed in [41]-[42]. The analysis of both controller is done through the help of graph along with bode plot analysis.

The main advantage of Novel PRESH controller proposed in this paper is to show that even if reference waveform is distorted one, this controller can tackle it in a very reliable manner and that is one of the originality of this proposed paper.

Novelty and Originality of the proposed research paper are as:

- Earlier technique develop for reducing current harmonics among researcher were lag-lead controller [16], modified PI controller [17]-[18], repetitive controller [19]-[20], dead beat controller [21], PR controller [22]-[23], shunt filter [24]-[28], suffer certain drawback as indicated:
- Standard PI controller, can't track ac current reference.
- [17]-[18], shows very poor response in tracking sinusoidal reference as well as while rejecting the disturbances.
- [22]-[23] can't use with present day Grid Connection as there is need of digital implementation of controller for rejecting the harmonic.
- Elimination of harmonic through the use of shunt filter becomes an old technique as their inability in eliminating the harmonic as per IEEE reference 519 and 1547.
- Design algorithm of Novel PRESH (Proportional Resonant Harmonic) controller is discussed where PRES controller is connected in parallel with RESH (Resonant Harmonic) controller instead of conventional series connection.
- Performance Analysis of Novel PRESH controller is discussed in term of harmonic mitigation with low computational load as compared to standard controller design.

The proposed paper is carved out in the following manner: Section II will discuss about three phase grid tied solar photovoltaic power system (SPPS), Section III will deal with design algorithm of Novel PRESH controller, Section IV will deal with performance analysis of Novel PRESH controller in term of Bode-Plot analysis and harmonic mitigation.

II. GRID-TIED THREE PHASE SPPS

Fig.1 represent the grid-tied three phase SPPS having Photovoltaic module/array, a capacitor C_{dc} and a 3- phase voltage source inverter connected with three phase grid having voltages as Vga, Vgb and Vgc.

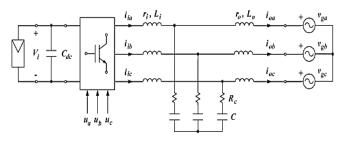


Fig.1 Grid -Tied Three Phase Solar Photovoltaic Power System (TPSPPS)

For reducing the high frequency switching harmonic, L-C-L filter is employed and for attenuating the peak magnitude of L-C-L filter at resonance frequency and damping resistor is made to connect in series with capacitor.

A. Open loop Mode (OLM) of 3-phase Grid-Tied TPSPPS

Average model of Grid-tied TPSPPS is indicated by Fig. 2. Input to this model is V_i which is a dc link voltage, grid voltage is denoted as V_g and control input d whose value varies from -1 to +1.

$$-1 \le \mathbf{d}_{\alpha} \le +1 \tag{1}$$

$$-1 \le d_\beta \le +1 \tag{2}$$

Inverter current is denoted by i_{i} and grid current is denoted by $i_{o}. \label{eq:io}$

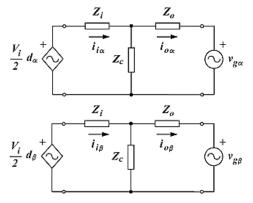


Fig. 2 Average circuit Model of Grid -tied 3-phase SPPS

The parameter used in the transfer function of above compensator is shown by Table 1, along with list of value used in the work. Table 1 also contain the detail parameter of grid tied three phase SPPS used here.

Symbol	Quantity	Nominal Value		
P _m	Maximum output power	3.2 KW		
f_i	Switching frequency	10 KHz		
V _{i, OC}	Open Circuit PV array output Voltage	750 V		
I _{i, SC}	Short circuit PV array output current	5.4 A		
V _{i, MP}	Maximum power PV output voltage	650 V		
I _{i, MP}	Maximum Power PV output current	4.9 A		
L _i	Inverter side Inductance	6.9 mH		
R _i	Inverter side resistance	0.27 Ώ		
С	Filter Capacitor	680 nF		
R _c	Filter damping resistance	6.8 Ώ		
L _o	Grid side inductance	2.1 mH		
R _o	Grid side resistance	0.14 Ώ		
Vg	Grid Voltage (rms, phase to neutral)	200 V		
f _o	Grid frequency	50 Hz		
K _p	Proportional gain	60 Ώ		
K _{i1}	Fundamental integral gain	300 Ώ s ²		
K _{in}	n-harmonic integral gain	300 Ω s ²		
ϵ_1	Fundamental damping factor	0.01		
En	n-harmonics damping factor	0.01		
N	Selected harmonics to be attenuated	5,7,11,13		
T _d	Control Processing delay time	100 µs		

Table 1 Parameters of Grid - Tied Three Phase SPPS

III. DESIGN ALGORITHM OF NOVEL PRESH CONTROLLER

A. Design Procedure of Novel PRESH controller

The main purpose of Novel PRESH controller is to achieve lower current harmonic distortion even if grid current experiences the abnormal conditions.

1st Step: Control Configuration of Novel PRESH controller

In this proposed Novel PRESH controller, shown by Fig. 3, inverter current is used as input to compensator of lower forward gain instead of applying the difference of reference current signal and grid current.

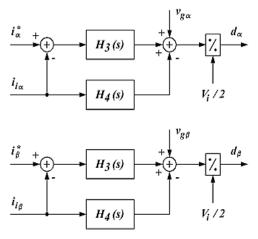


Fig. 3 Proposed Novel PRESH Controller

Where, $H_3(s)$ is PRES (Proportional Resonant) controller and $H_4(s)$ is RESH (Resonant Harmonic) Controller.

Transfer functions of Novel PRESH controller are obtained as:

$$H_{3}(s) = \frac{k_{i1}2\xi_{1}\omega_{o}s}{s^{2}+2\xi_{1}\omega_{o}s+\omega_{o}^{2}}$$
(3)
$$H_{4}(s) = k_{p} + \sum_{n} \frac{k_{in}2\xi_{n}(n\omega_{o})s}{s^{2}+2\xi_{n}(n\omega_{o})s+(n\omega_{o})^{2}}$$
(4)

2nd Step: Closed-Loop Transfer Function of Novel PRESH controller

From Fig. 3, control input of proposed Novel PRESH controller can be written as

$$d_{\alpha} = \frac{2}{V_{i}} \left[v_{g\alpha} + H_{3}(s)(i_{\alpha}^{*} - i_{i\alpha}) - H_{4}(s)i_{i\alpha} \right] H_{d}(s)$$
(5)

$$d_{\beta} = \frac{2}{v_i} \left[v_{g\beta} + H_3(s) \left(i_{\beta}^* - i_{i\beta} \right) - H_4(s) i_{i\beta} \right] H_d(s)$$
(6)

By substituting equation (5) and (6) in Fig. 2 so as to obtain closed loop transfer function of proposed Novel PRESH controller, we get:

$$T(s) = (Z_c(s) + Z_o(s))G_i(s)(H_3(s) + H_4(s)) \times H_d(s)$$
(7)

$$G_r(s) = \frac{Z_c(s)G_i(s)H_3(s)H_d(s)}{1+T(s)}$$
(8)

$$G_g(s) = -\frac{G_i(s) \left(Z_i(s) + (H_3(s) + H_4(s)) H_d(s) \right)}{1 + T(s)} \tag{9}$$

IV. ANALYSIS OF NOVEL PRESH CONTROLLER

A. Bode Plot Analysis of proposed Novel PRESH Controller

Bode Plot figure of PRES and RESH controller used in proposed Novel PRESH Controller is indicated by Fig. 4.

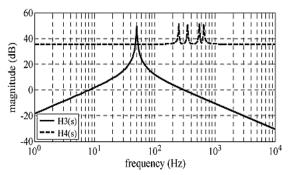


Fig. 4. Bode-Plot figure of PRES and RESH Controller used in Proposed Novel PRESH controller

Bode-Plot of transfer function for proposed Novel PRESH controller is indicated by Fig. 5. All the value taken for case study of Novel PRESH controller is indicated in table 1.

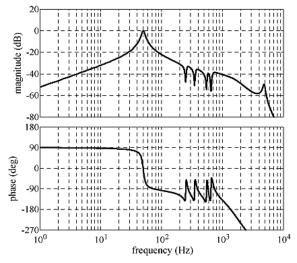


Fig. 5. Bode-Plot figure of proposed Novel PRESH controller

From the above figure it is clear that, transfer function of proposed Novel PRESH controller has behavior similar to band pass filter. At, selected harmonic frequency i.e 250, 350, 550 and 650 Hz, dip in magnitude diagram can be seen. Thus, by using this proposed Novel PRESH controller, tracking of fundamental component of signal can be expected even if reference waveform is distorted one.

B. Harmonic Analysis of proposed Novel PRESH Controller in three phase SPPS

The proposed Novel PRESH Controller of this paper is tested with high voltage disturbances of the grid and the waveform of various parameter used is shown by Fig. 6.

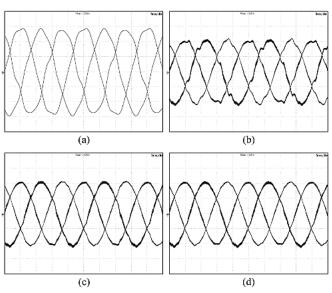


Fig. 6 Simulation result for grid condition with high voltage THD (a) Grid Voltage (b) Standard Control without Resonant Harmonic Controller (c) Standard Control with Resonant Harmonic Controller (d) Proposed Novel PRESH Controller

Harmonic distortion thus calculated is depicted by table 2 comparing the THD without controller and with proposed controller of the system.

Table 2 THD under different control technique

Control Technique	Vga	$\mathbf{V}_{\mathbf{g}\mathbf{b}}$	Vgc	Ioa	Iob	Ioc
Standard control without Resonant Harmonic Controller	4.0	4.5	7.5	3.6	3.4	5.7
Standard control with Resonant Harmonic Controller	3,7	4.7	7.8	1.8	1.8	2.3
Proposed Novel PRESH Controller	3.8	4.8	7.8	0.9	0.92	1.12

From table 2, it is cleared that standard control gives very poor performance when resonant harmonic controller is not engaged however, the performance improves a bit after engaging the resonant harmonic controller.

The Proposed Novel PRESH controller reduces the current harmonic distortion well below to 5% which is as per IEEE standard 519 and 1547.

V. CONCLUSION

The presented paper discuss the design algorithm and performance analysis in term of THD and harmonic mitigation along with bode plot analysis of Novel PRESH (PRES+RESH) controller Interfaced with three phase SPPS (Solar Photovoltaic Power System).

Proposed Novel PRESH Controller was also tested with high voltage disturbances of the grid and waveform shows good result.

Further, identification of indirect mechanism is done that was responsible in generating the current harmonic from abnormal

grid condition in standard resonant current controller

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References

- [1] E. Figueres, G. Garcerá, J. Sandia, F. González-Espin, and J. C. Rubio, "Sensitivity study of the dynamics of three-phase photovoltaic inverters with an LCL grid filter," IEEE Trans. Ind. Electron., vol. 56, no. 3, pp. 706– 717, Mar. 2009.
- [2] M. C. Cavalcanti, K. C. de Oliveira, A. M. de Farias, F. A. Neves, G. M. Azevedo, and F. C. Camboim, "Modulation techniques to eliminate leakage currents in transformerless three-phase photovoltaic systems," IEEE Trans. Ind. Electron., vol. 57, no. 4, pp. 1360–1368, Apr. 2010.
- [3] M. Cacciato, A. Consoli, R. Attanasio, and F. Gennaro, "Soft-switching converter with HF transformer for gridconnected photovoltaic systems," IEEE Trans. Ind. Electron., vol. 57, no. 5, pp. 1678–1686, May 2010.
- [4] S. V. Araujo, P. Zacharias, and R. Mallwitz, "Highly efficient single-phase transformerless inverters for gridconnected photovoltaic systems," IEEE Trans. Ind. Electron., vol. 57, no. 9, pp. 3118–3128, Sep. 2010.
- [5] R. Kadri, J. P. Gaubert, and G. Champenois, "An improved maximum power point tracking for photovoltaic grid-connected inverter based-on voltage oriented control," IEEE Trans. Ind. Electron., vol. 58, no. 1, pp. 66–75, Jan. 2011.
- [6] A. I. Bratcu, I. Munteanu, S. Bacha, D. Picault, and B. Raison, "Cascaded dc-dc converter photovoltaic systems: Power optimization issues," IEEE Trans. Ind. Electron., vol. 58, no. 2, pp. 403–411, Feb. 2011.
- [7] Q. Mai, M. Shan, L. Liu, and J. M. Guerrero, "A novel improved variable step-size incremental-resistance MPPT method for PV systems," IEEE Trans. Ind. Electron., vol. 58, no. 6, pp. 2427–2434, Jun. 2011.
- [8] N. A. Rahim, K. Chaniago, and J. Selvaraj, "Single-phase sevenlevel grid-connected inverter for photovoltaic system," IEEE Trans. Ind. Electron., vol. 58, no. 6, pp. 2435–2443, Jun. 2011.
- [9] I. J. Balaguer, Q. Lei, S. Yang, U. Supatti, and F. Z. Peng, "Control for grid-connected and intentional islanding operations of distributed power generation," IEEE Trans. Ind. Electron., vol. 58, no. 1, pp. 147–157, Jan. 2011.
- [10] J. M. Guerrero, J. C. Vasquez, J. Matas, L. Garcia de Vicuna, and M. Castilla, "Hierarchical control of droopcontrolled ac and dc microgrids—A general approach toward standardization," IEEE Trans. Ind. Electron., vol. 58, no. 1, pp. 158–172, Jan. 2011.
- [11] C. Yuen, A. Oudalov, and A. Timbus, "The provision of frequency control reserves from multiple microgrids," IEEE Trans. Ind. Electron., vol. 58, no. 1, pp. 173–183, Jan. 2011.
- [12] Characteristics of the Utility Interface for Photovoltaic (PV) Systems, IEC61727, Dec. 2004.
- [13] IEEE Standard for Interconnecting Distributed Resources With Electric Power Systems, IEEE15471, 2008.
- [14] 2002 National Electrical Code, Nat. Fire Protection Assoc., Inc., Quincy, MA, 2002.
- [15] Thomas Basso and N. Richard Friedman (November 2003) <u>IEEE 1547 National Standard for Interconnecting</u> <u>Distributed Generation: How Could It Help My Facility?</u>

- [16] D. Shetty e N. Prabhu, "Ziegler-Nichols method based VAR current controller for static compensator", Energy Procedia, vol. 117, p. 543–550, jun. 2017.
- [17] M. T. Faiz, M. M. Khan, X. Jianming, S. Habib, e H. Tang, "Double feed-forward compensation based true damping of Inductor-capacitor-Induxtor type grid tied lainverter", in 2018 IEEE International Conference on Industrial Technology (ICIT), 2018, p. 788–793.
- [18] Y. S. Kuo, J. Y. Lin, J. C. Tang, e J. G. Hsieh, "Lead-lag compensator design based on vector margin and steadystate error of the step response via particle swarm optimization", in 2016 International Conference on Fuzzy Theory and Its Applications (iFuzzy), 2016, p. 1–6.
- [19] L. Wang, N. Ertugrul, e M. Kolhe, "Evaluation of died beat CC for grid tied converters", in IEEE PES Innovative Smart Grid Technologies, 2012, p. 1–7.
- [20] J. N. da Silva, A. J. S. Filho, D. A. Fernandes, A. P. N. Tahim, E. R. C. da Silva, e F. F. Costa, "A discrete current controller for 1-phase grid-tied inverters", in 2017 Brazilian Power Electronics Conference (COBEP), 2017, p. 1–6.
- [21]K. Zhou, F. Blaabjerg, D. Wang, e Y. Yang, Periodic Control of PEC, IET, 2016.
- [22] M. Castilla, J. Miret, J. Matas, L. Garcia de Vicuna, and J. M. Guerrero, "Control design guidelines for single-phase grid-connected photovoltaic inverters with damped resonant harmonics compensators," IEEE Trans. Ind. Electron., vol. 56, no. 11, pp. 4492–4501, Nov. 2009.
- [23] M. Liserre, R. Teodorescu, and F. Blaabjerg, "Stability of photovoltaic and wind turbine grid-connected inverters for a large set of grid impedance values," IEEE Trans. Power Electron., vol. 21, no. 1, pp. 263–272, Jan. 2006.
- [24] F. Blaabjerg, R. Teodorescu, M. Liserre, and A. V. Timbus, "Overview of control and grid synchronization for distributed power generation systems," IEEE Trans. Ind. Electron., vol. 53, no. 5, pp. 1398–1409, Oct. 2006.
- [25] Yadav U., Gupta A., Rai H.K., Bhalla D.K (2021) Mitigation of Harmonic Current in Grid Connected Solar Power System. In: Muzammil M., Chandra A., Kankar P.K, Kumar H. (eds) Recent Advances in Mechanical Engineering. Lecture Notes in Mechanical Engineering. Springer, Singapore.
- [26] P. Salmeron and S. Litran, "Improvement of the Electric Power Quality Using Series Active and Shunt Passive Filters," IEEE Transactions on Power Delivery, vol. 25, no. 2, pp. 1058-1067, 2010.
- [27] Q. Trinh and H. Lee, "An Advanced Current Control Strategy for Three-Phase Shunt Active Power Filters," IEEE Transactions on Industrial Electronics, vol. 60, no. 12, pp. 5400-5410, 2013
- [28] Jinwei He, Yun Wei Li, F. Blaabjerg and Xiongfei Wang, "Active Harmonic Filtering Using Current-Controlled, Grid-Connected DG Units with Closed-Loop Power Control," IEEE Transaction.
- [29] L. B. G. Campanhol, S. A. O. Silva, L. P. Sampaio, A. A O. Junior, "A Grid-Connected Photovoltaic Power System with Active Power Injection, Reactive Power Compensation and Harmonic Filtering," in Proc. of COBEP, pp.642-649, 2013.
- [30] U. Yadav and A. Gupta, "Current Harmonic Mitigation in Grid Tied Solar photovoltaic System via PRES" 2020 5th IEEE International Conference on Recent Advances and Innovations in Engineering (ICRAIE), Jaipur, India, 2020, pp. 1-5.
- [31] U. Yadav, A. Gupta and R. Ahuja, "Robust Control Design Procedure and Simulation of PRES Controller having phase-Locked Loop (PLL) control technique in Grid-Tied Converter" 2020 3rd International Seminar on Research of Information Technology and Intelligent Systems (ISRITI) yogakarta, Indonesia, 2020, pp. 445-450.

- [32] A. S. Padula, E. J. Agnoletto, R. V. A. Neves, R. F. Q. Magossi, R. Q. Machado and V. A. Oliveira, "Partial Harmonic Current Distortion Mitigation in Microgrids Using Proportional Resonant Controller," 2019 18th European Control Conference (ECC), Naples, Italy, 2019, pp. 435-440.
- [33] Althobaiti, M. Armstrong, M. A. Elgendy and F. Mulolani, "Three-phase grid connected PV inverters using the proportional resonance controller," 2016 IEEE 16th International Conference on Environment and Electrical Engineering (EEEIC), Florence, Italy, 2016, pp. 1-6.
- [34] P. S. Prasad and A. M. Parimi, "Harmonic Mitigation in Grid Connected and Islanded Microgrid Via Adaptive Virtual Impedance," 2020 IEEE International Conference on Power Electronics, Smart Grid and Renewable Energy (PESGRE2020), Cochin, India, 2020, pp. 1-6.
- [35] Jeong, H.-G.; Kim, G.-S.; Lee, K.-B. Second-Order Harmonic Reduction Technique for Photovoltaic Power Conditioning Systems Using a Proportional-Resonant Controller. Energies 2013, 6, 79–96.
- [36] I. Etxeberria-Otadui, U. Viscarret, M. Caballero, A. Rufer, and S. Bacha, "New optimized PWM VSC control structures converter under unbalanced input voltage transients," IEEE Trans. Ind. Electron., vol. 54, no. 5, pp. 2902–2914, Oct. 2007.
- [37] P. Rodríguez, J. Pou, J. Bergas, J. I. Candela, R. P. Burgos, and D. Boroyevich, "Decoupled double synchronous reference frame PLL for power converters control," IEEE Trans. Power Electron., vol. 22, no. 2, pp. 584–592, Mar. 2007.
- [38] F. Wang, J. L. Duarte, and M. A. Hendrix, "Pliant active and reactive power control for grid-interactive converters under unbalanced voltage dips," IEEE Trans. Power Electron., vol. 26, no. 5, pp. 1511–1521, May 2011.
- [39] P. Rodriguez, A. V. Timbus, R. Teodorescu, M. Liserre, and F. Blaabjerg, "Flexible active power control of distributed power generation systems during grid faults," IEEE Trans. Ind. Electron., vol. 54, no. 5, pp. 2583– 2592, Oct. 2007.
- [40] P. Rodriguez, A. V. Timbus, R. Teodorescu, M. Liserre, and F. Blaabjerg, "Reactive power control for improving wind turbine system behavior under grid faults," IEEE Trans. Power Electron., vol. 24, no. 7, pp. 1798–1801, Jul. 2009.
- [41] P. Rodriguez, A. Luna, I. Candela, R. Mujal, R. Teodorescu, and F. Blaabjerg, "Multiresonant frequencylocked loop for grid synchronization of power converters under distorted grid condition.
- [42] M. Castilla, J. Miret, J. Matas, L. Garcia de Vicuna, and J. M. Guerrero, "Linear current control scheme with series resonant harmonic compensator for single-phase gridconnected photovoltaic inverters," IEEE Trans. Ind. Electron., vol. 55, no. 7, pp. 2724–2733, Jul. 2008.
- [43] J. Miret, M. Castilla, J. Matas, J. M. Guerrero, and J. C. Vasquez, "Selective harmonic-compensation control for single-phase active power filter with high harmonic rejection," IEEE Trans. Ind. Electron., vol. 56, no. 8, pp. 3117–3127, Aug. 2009.
- [44] A. G. Yepes, F. D. Freijedo, J. Doval-Gandoy, O. Lopez, J. Malvar, and P. Fernandez-Comesana, "Effects of discretization methods on the performance of resonant controllers," IEEE Trans. Power Electron., vol. 25, no. 7, pp. 1692–1712, Jul. 2010.

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