# A New Class of Monotone Functions of the Residue Number System 

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#### Abstract

This paper presents a new class of monotone functions that can be computed from the Residue Number System (RNS) to the integers. On the basis of these functions new implementations are proposed for residue-to-binary conversion and magnitude comparison that are superior to traditional techniques, if a modulus of the kind $2^{\mathrm{k}}$ ( k integer) is included in the set of RNS moduli.


Keywords - Chinese Remainder Theorem, Magnitude Comparison, Multi-operand Modular Adder, Residue Number System, Residue-to-Binary Conversion.

## I. Introduction

$I_{i}^{N}$N the Residue Number System (RNS), the effective implementation of non-modular operations like residue-tobinary conversion and magnitude comparison is mandatory. Residue-to-binary conversion is necessary for the use of RNS arithmetic units into general purpose computers, which are based on the binary number system. Magnitude comparison supports other logic operations that are complex in the RNS due to the difficulty in defining an order relation on quotient sets [12].
The main techniques for the implementation of non-modular operations use the Mixed-Radix Conversion (MRC) - which is strictly sequential, or the Chinese Remainder Theorem (CRT) which is more attractive since it provides a parallel conversion formula [13].
Other techniques have been proposed which use functions defined from the RNS to the integers. The 'diagonal function' exploits the observation that the integers in residue representation dispose themselves on diagonals when they are arranged in the multi-dimensional discrete space associated to the RNS [2, 3]. Unfortunately, although the 'diagonal function' is a powerful tool for magnitude comparison, it does not support residue-to-binary conversion [2,3].
This paper introduces a new class of monotone functions that support effectively both magnitude comparison in RNS and residue-to-binary conversion. Through the paper an effective scheme for the computation of the new functions is presented and the superiority of the new implementations of magnitude comparison and residue-to-binary conversion with respect to

[^0]traditional techniques is demonstrated.
The organisation of the paper is the following: Section 2 introduces the new functions and presents the scheme to compute them directly from the RNS. Section 3 shows the new implementations of residue-to-binary conversion and magnitude comparison. The comparative analysis of the performance of the new techniques and the traditional approaches is reported in Section 4. Section 5 presents a case study.

## II. Monotone Functions of the RNS

In the RNS based on the pairwise relatively prime moduli $m_{1}, m_{2}, \ldots, m_{N}$, an integer $X \in[0, M-1] \quad\left(M=m_{1} \cdot m_{2} \cdot \ldots \cdot m_{N}\right)$ is uniquely represented by the N -tuple $\left(\mathrm{x}_{1}, \mathrm{x}_{2}, \ldots, \mathrm{x}_{\mathrm{N}}\right)$, where $\mathrm{x}_{\mathrm{i}}=|\mathrm{X}|_{\mathrm{mi}}$ is the residue of X modulo mi , $\mathrm{i}=1,2, \ldots, \mathrm{~N}$ [12]. Let be $\mathrm{I} \subset\{1,2, . ., \mathrm{N}\}, \mathrm{I} \neq \varnothing$, the function $\mathrm{F}_{\mathrm{I}}$ proposed in this paper is:

$$
\begin{equation*}
\mathrm{F}_{\mathrm{I}}(X)=\sum_{i \in I}\left[\frac{X}{m_{i}}\right] \tag{1}
\end{equation*}
$$

where $[a]$ denotes the largest integer not exceeding $a$.
Theorem 1 shows an effective scheme to compute $\mathrm{F}_{\mathrm{I}}(\mathrm{X})$ directly from the RNS representation of X.

## Theorem 1:

Let $\mathrm{m}_{1}, \mathrm{~m}_{2}, . ., \mathrm{m}_{\mathrm{N}}$ be the set of pairwise relatively prime moduli of the RNS, let $\mathrm{I} \subset\{1,2, . ., \mathrm{N}\}, \mathrm{I} \neq \varnothing$, and

$$
\begin{gathered}
\mathrm{M}_{\mathrm{i}}=\frac{M}{m_{i}} \\
M_{I}=\sum_{i \in I} M_{i} \\
S_{I N V}=\sum_{i \in I}\left|\frac{1}{m_{i}}\right|_{M_{I}}
\end{gathered}
$$

where

$$
\left|\frac{1}{m_{i}}\right|_{M_{I}}
$$

is the multiplicative inverse of $\mathrm{m}_{\mathrm{i}}$ modulo $\mathrm{M}_{\mathrm{I}}$, $\mathrm{i} \in \mathrm{I}$ [12]. If $\left(\mathrm{x}_{1}, \mathrm{x}_{2}, \ldots, \mathrm{x}_{\mathrm{N}}\right)$ is the RNS representation of $\mathrm{X} \in[0, \mathrm{M}-1]$, the value $\mathrm{F}_{\mathrm{I}}(\mathrm{X})$ can be computed as:

$$
\begin{equation*}
\mathrm{F}_{\mathrm{I}}(X)=\left|\sum_{i=1}^{N} b_{i} \cdot x_{i}\right|_{M_{I}} \tag{2}
\end{equation*}
$$

with:

$$
\begin{aligned}
& b_{i}=-\left|\frac{1}{m_{i}}\right|_{M_{I}}, i \in I \\
& b_{j}=\left.\left.\left|M_{j} \cdot S_{I N V} \cdot\right| \frac{1}{M_{j}}\right|_{m_{j}}\right|_{M_{I}}, j \in J
\end{aligned}
$$

with

$$
J=\{1,2, \ldots, N\}-I \quad \text { (hence } I \cup J=\{1,2, \ldots, N\} \text { and } I \cap J=\varnothing \text { ). }
$$

Proof: See Figure 1.
Moreover, the coefficients $\mathrm{b}_{\mathrm{k}}, \mathrm{k} \in\{1,2, \ldots, N\}$ are well defined if there exist the multiplicative inverses

$$
\left|\frac{1}{M_{j}}\right|_{m_{j}}, j \in J
$$

and

$$
\left|\frac{1}{m_{i}}\right|_{M_{I}}, i \in I
$$

This is true if and only if $\mathrm{m}_{\mathrm{j}}, \mathrm{M}_{\mathrm{j}}$ and $\mathrm{m}_{\mathrm{i}}, \mathrm{M}_{\mathrm{I}}$ are couples of relatively prime integers [1]. This is shown in Theorem 2.

## Theorem 2:

Let $m_{1}, m_{2}, \ldots, m_{N}$ be the set of pairwise relatively prime moduli of the RNS and $\mathrm{I} \subset\{1,2, . ., \mathrm{N}\}, \mathrm{I} \neq \varnothing$. Let

$$
\begin{array}{r}
M=\prod_{i=1}^{N} m_{i} \\
\mathrm{M}_{\mathrm{i}}=\frac{M}{m_{i}}
\end{array}
$$

and

$$
M_{I}=\sum_{i \in I} M_{i}
$$

the following conditions are true:
(a) $\mathrm{m}_{\mathrm{j}}$ and $\mathrm{M}_{\mathrm{j}}$ are relatively prime;
(b) $\mathrm{m}_{\mathrm{i}}$ and $\mathrm{M}_{\mathrm{I}}$ are relatively prime.

Proof: See Figure 2.
Example: For the RNS of moduli $\mathrm{m}_{1}=37, \mathrm{~m}_{2}=41, \mathrm{~m}_{3}=43$, $\mathrm{m}_{4}=64$, if $\mathrm{I}=\{2,4\}$, it results that $\mathrm{M}_{\mathrm{I}}=\mathrm{M}_{2}+\mathrm{M}_{4}=101824+65231=$ 167055 and $b(1)=9030, \quad b(2)=8149, \quad b(3)=27195$, $b(4)=122681$. Now, let $X=17435 \rightarrow($ RNS $)(12,23,19,7)$, we have:

- from eq. 1:

$$
\mathrm{F}_{\mathrm{I}}(\mathrm{X})=\left[\frac{X}{m_{2}}\right]+\left[\frac{X}{m_{4}}\right]=709
$$

- from eq. 2:

$$
\mathrm{F}_{\mathrm{I}}(\mathrm{X})=\quad \mathrm{F}_{\mathrm{I}}(X)=\left|\sum_{i=1}^{N} b_{i} \cdot x_{i}\right|_{M_{I}}=709
$$

## III. Monotone Functions for Non-Modular Operations IN THE RNS

Let $\mathrm{m}_{1}, \mathrm{~m}_{2}, \ldots, \mathrm{~m}_{\mathrm{N}}$ be the set of relatively prime moduli of the RNS and let be

$$
M=\prod_{i=1}^{N} m_{i}
$$

The new implementations of magnitude comparison and residue-to-binary conversion are reported in the following [4].

## 1) Magnitude Comparison.

Let $\mathrm{X}, \mathrm{Y} \in[0, \mathrm{M}-1]$ be two integers whose RNS representation is $\mathrm{X} \rightarrow\left(\mathrm{x}_{1}, \mathrm{x}_{2}, \ldots, \mathrm{x}_{\mathrm{N}}\right)$ and $\mathrm{Y} \rightarrow\left(\mathrm{y}_{1}, \mathrm{y}_{2}, \ldots, \mathrm{y}_{\mathrm{N}}\right)$, respectively. From (1) we have $\mathrm{X}<\mathrm{Y} \Rightarrow \mathrm{F}_{\mathrm{I}}(\mathrm{X})<\mathrm{F}_{\mathrm{I}}(\mathrm{Y})$ or $\left(\mathrm{F}_{\mathrm{I}}(\mathrm{X})=\mathrm{F}_{\mathrm{I}}(\mathrm{Y})\right.$ and $\mathrm{x}_{\mathrm{i}}<\mathrm{y}_{\mathrm{i}}$, $\mathrm{i} \in \mathrm{I}$ ). In fact, since

$$
\begin{aligned}
& X=\left[\frac{X}{m_{i}}\right] \cdot m_{i}+x_{i} \\
& Y=\left[\frac{Y}{m_{i}}\right] \cdot m_{i}+y_{i}
\end{aligned}
$$

Proof of Theorem 1
From

$$
x_{i}=X-m_{i} \cdot\left[\frac{X}{m_{i}}\right]
$$

it follows that:

$$
\begin{gathered}
\left|\sum_{i=1}^{N} b_{i} \cdot x_{i}\right|_{M_{I}}=\left\lvert\, \sum_{i=1}^{N} b_{i} \cdot\left(X-\left.m_{i} \cdot\left[\frac{X}{m_{i}}\right]\right|_{M_{I}}=\left|\sum_{i=1}^{N} b_{i} \cdot X-\sum_{i=1}^{N} b_{i} \cdot m_{i} \cdot\left[\frac{X}{m_{i}}\right]\right|_{M_{I}}\right.\right. \\
\left.\left|-S_{I N V} \cdot X+S_{I N V} \cdot X \cdot\right| \sum_{j \in J}\left|\frac{1}{M_{j}}\right|_{m_{j}}\right|_{i \in I}+\sum_{i \in I}\left[\frac{X}{m_{i}}\right]-\sum_{j \in J} S_{I N V} \cdot X-\left.\sum_{j \in J}\left(M_{j} \cdot m_{j}\right) \cdot S_{I N V} \cdot\left|\frac{1}{M_{j}}\right|_{m_{j}} \cdot\left[\frac{X}{m_{j}}\right]\right|_{M_{I}=} \\
\left.\left.\left|X \cdot S_{I N V} \cdot\left(-1+\sum_{j \in J} M_{j} \cdot\left|\frac{1}{M_{j}}\right|_{m_{j}}\right)+\sum_{i \in I}\left[\frac{X}{m_{i}}\right]-\sum_{j \in J} M \cdot S_{I N V} \cdot\right| \frac{1}{M_{j}}\right|_{m_{j}} \cdot\left[\frac{X}{m_{j}}\right]\right|_{M_{I}} \\
\left|0+\sum_{i \in I}\left[\frac{X}{m_{i}}\right]-0\right|_{M_{I}}=\left|\sum_{i \in I}\left[\frac{X}{m_{i}}\right]\right|_{M_{I}}=\mathrm{F}_{\mathrm{I}}(\mathrm{X}) .
\end{gathered}
$$

Figure 1. Proof of Theorem 1
Proof of Theorem 2
(a) Assuming that $\mathrm{m}_{\mathrm{j}}$ and $\mathrm{M}_{\mathrm{j}}$ are not relatively prime, since

$$
M_{j}=m_{1} \cdot m_{2} \cdot \ldots \cdot m_{j-1} \cdot m_{j+1} \cdot \ldots m_{N}
$$

a modulus $\mathrm{m}_{\mathrm{i}}$ must exist, $\mathrm{i}=1,2, . ., \mathrm{N}, \mathrm{i} \neq \mathrm{j}$, which is not relatively prime with $\mathrm{m}_{\mathrm{j}}$. This contradicts the hypothesis.
(b) Assuming that $m_{\mathrm{i}}$ and $\mathrm{M}_{\mathrm{I}}$ are not relatively prime, it follows that three integers $\bar{\alpha}, \bar{\beta}, \bar{\delta}$ exist so that $m_{i}=\bar{\alpha} \cdot \bar{\beta}$ and $M_{I}=\bar{\alpha} \cdot \bar{\delta}$ (with $\bar{\alpha} \neq 1$ ). Now, since

$$
M_{I}=\sum_{k \in I} M_{k}=\sum_{k \in I} \frac{M}{m_{k}}=\left(\frac{M}{m_{i}}+m_{i} \cdot \sum_{\substack{k \in I \\ k \neq i}} \frac{M_{i}}{m_{k}}\right)
$$

substituting $m_{i}=\bar{\alpha} \cdot \bar{\beta}$ and $M_{I}=\bar{\alpha} \cdot \bar{\delta}$ in (3) we obtain

$$
\bar{\alpha} \cdot \bar{\delta}=\frac{M}{m_{i}}+\bar{\alpha} \cdot \bar{\beta} \cdot \sum_{\substack{k=I \\ k \neq i}} \frac{M_{i}}{m_{k}} \quad \Rightarrow \quad \bar{\alpha} \cdot\left(\bar{\delta}-\bar{\beta} \cdot \sum_{\substack{k=I \\ k \neq i}} \frac{M_{i}}{m_{k}}\right)=\frac{M}{m_{i}}=\prod_{\substack{k=1 \\ k \neq i}}^{N} m_{k}
$$

Thus, a modulus $\mathrm{m}_{\mathrm{k}}$ exists, $\mathrm{k} \neq \mathrm{i}$, so that $\bar{\alpha}$ divides $\mathrm{m}_{\mathrm{k}}$. This means that $m_{k}$ and $m_{i}$ are not relatively prime moduli. This contradicts the hypothesis.
Q.E.D.

Figure 2: Proof of Theorem 2

Table I. Performance Analysis

|  |  | Time Complexity | ROM | $\begin{gathered} \text { R-to-B } \\ \text { Conversion } \end{gathered}$ | Magnitude comparison |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Technique |  |  |  |  |  |
| Parallel Techniques | CRT (see [13]) | $\mathrm{O}(\operatorname{logN})$ | $\Omega\left(\mathrm{N}^{2}\right)$ | Y | Y |
|  | D (see [2]) | $\mathrm{O}(\operatorname{logN})$ | $\Omega\left(\mathrm{N}^{2}\right)$ | Not supported | Y |
|  | $\mathrm{D}_{\mathrm{k}}$ (see [3]) | $\mathrm{O}(\operatorname{logN})$ | $\Omega\left(\mathrm{N}^{2}\right)$ | Not supported | Y |
|  | $\mathrm{F}_{\text {I }}$ | $\mathrm{O}(\operatorname{logN})$ | $\Omega\left(\mathrm{N}^{2}\right)$ | Y | Y |

it follows that if $\mathrm{X}<\mathrm{Y}$ and $\mathrm{F}_{\mathrm{I}}(\mathrm{X})=\mathrm{F}_{\mathrm{I}}(\mathrm{Y})$ it results that $\mathrm{x}_{\mathrm{i}}<\mathrm{y}_{\mathrm{i}}, \mathrm{i} \in \mathrm{I}$. Therefore, magnitude comparison can be performed as follows:

## STEP 1. Compute $\boldsymbol{F}_{\boldsymbol{I}}(X)$ and $\boldsymbol{F}_{\boldsymbol{I}}(Y)$

STEP 2. Compare $\boldsymbol{F}_{\boldsymbol{I}}(X)$ and $\boldsymbol{F}_{\boldsymbol{I}}(Y)$; if $\boldsymbol{F}_{\boldsymbol{I}}(X)=\boldsymbol{F}_{\boldsymbol{I}}(Y)$ then compare $x_{i}$ and $y_{i}$.

## 2) Residue-to-binary Conversion.

Let be $I=\{i\}$, from (1) we have

$$
\mathrm{F}_{\mathrm{I}}(X)=\left[\frac{X}{m_{i}}\right]
$$

Now, since

$$
X=m_{i} \cdot\left[\frac{X}{m_{i}}\right]+x_{i}
$$

it results:

$$
\begin{equation*}
X=m_{i} \cdot F_{I}(X)+x_{i} \tag{3}
\end{equation*}
$$

If the modulus $m_{i}$ is a power of 2, i.e. $m_{i}=2^{\mathrm{k}}$ (k integer), the implementation of eq. (3) implies shift-left operation rather than ordinary multiplication and the binary representation of X is obtained by concatenating the binary representations of $\mathrm{FI}(\mathrm{X})$ (most significant bits of X ) and $x_{i}$ (least significant bits of X ). In this case X is obtained as follows:

STEP 1. Compute $\boldsymbol{F}_{I}(X)$, then do $X=\boldsymbol{F}_{I}(X) \cup x_{i}$;
(where $\boldsymbol{F}_{I}(X) \cup x_{i}$ is the concatenation of the binary representations of $\boldsymbol{F}_{I}(X)$ and $\left.x_{i}\right)$.

Example: For the RNS of moduli $m_{1}=37, m_{2}=41, m_{3}=43$, $\mathrm{m}_{4}=64$, if $\mathrm{I}=\{4\}$, it results that $\mathrm{M}_{\mathrm{I}}=\mathrm{M}_{4}=65231$ and $\mathrm{b}(1)=3526$, $b(2)=3182, \quad b(3)=10619, \quad b(4)=47904$. Now, let
$X=1119797 \rightarrow($ RNS $)(29,5,34,53) \quad$ and $\quad Y=432163 \rightarrow$
(RNS)(3,23,13,35) we have:

## * Magnitude Comparison.

Since $F_{I}(X)=17496$ and $F_{I}(Y)=4714$, from $F_{I}(X)>F_{I}(Y)$ it follows that $\mathrm{X}>\mathrm{Y}$.

## * Residue-to-Binary Conversion.

Since the binary representations of $\mathrm{F}_{\mathrm{I}}(\mathrm{X})=17496$ and $x_{4}=53$ are 100010001011000 and 110101 , respectively, it results that the binary representation of $\mathrm{X}=1119797$ is $100010001011000 \cup 110101=100010001011000110101$. Analogously, since the binary representations of $\mathrm{F}_{\mathrm{I}}(\mathrm{Y})=4714$ and $\mathrm{y}_{4}=35$ are 1101001100000 and 100011 , respectively, it results that the binary representation of $\mathrm{Y}=432163$
is
$1101001100000 \cup 100011=1101001100000100011$.

## IV. Performance Analysis

Let $\mathrm{m}_{1}, \mathrm{~m}_{2}, \ldots, \mathrm{~m}_{\mathrm{N}}$ be the set of relatively prime moduli of the RNS $M=m_{1} \cdot m_{2} \cdot \ldots \cdot m_{N}$ and let $\mathrm{X} \in[0, \mathrm{M}-1]$ be an integer whose RNS representation is $\mathrm{X} \rightarrow\left(\mathrm{x}_{1}, \mathrm{x}_{2}, \ldots, \mathrm{x}_{\mathrm{N}}\right)$.

## - Mixed Radix Conversion (MRC).

The MRC is based on the formula [13]:

$$
\begin{equation*}
\mathrm{X}=\mathrm{a}_{1}+\mathrm{m}_{1} \mathrm{a}_{2}+\mathrm{m}_{1} \mathrm{~m}_{2} \mathrm{a}_{3}+\ldots+\mathrm{m}_{1} \mathrm{~m}_{2} \ldots \mathrm{~m}_{\mathrm{N}-1} \mathrm{a}_{\mathrm{N}} \tag{4}
\end{equation*}
$$

where $a_{1}, a_{2}, \ldots a_{N}$ are the Mixed Radix digits, which can be obtained recursively:

$$
\begin{equation*}
a_{1}=x_{1}, a_{2}=\left(X-a_{1}\right) / m_{1}, \ldots \tag{5}
\end{equation*}
$$

## - Chinese Remainder Theorem (CRT).

The CRT is based on the conversion formula [13]:

$$
\begin{equation*}
X=\left|\sum_{i=1}^{N} N_{i} \cdot x_{i}\right|_{M} \tag{6}
\end{equation*}
$$

where

$$
\begin{gathered}
N_{i}=M_{i} \cdot\left|\frac{1}{M_{i}}\right|_{m} \\
M_{i}=\frac{M}{m_{i}}
\end{gathered}
$$

## - Diagonal Function (D).

The 'diagonal function' of the RNS of moduli $\mathrm{m}_{1}, \mathrm{~m}_{2}, \ldots, \mathrm{~m}_{\mathrm{N}}$, is defined as [2]:

$$
\begin{equation*}
D(X)=\left|\sum_{i=1}^{N} k_{i} \cdot x_{i}\right|_{S Q} \tag{7}
\end{equation*}
$$

where:

$$
S Q=\sum_{i=1}^{N} M_{i}
$$

is the 'diagonal modulus' of the $\operatorname{RNS}\left(\mathrm{M}_{\mathrm{i}}=\mathrm{M} / \mathrm{m}_{\mathrm{i}}, \mathrm{i}=1,2, \ldots, \mathrm{~N}\right)$ and $k_{i}$ is the multiplicative inverse of $m_{i}$ modulo $S Q$ ).

## - Diagonal Function by reduction of the RNS space dimensionality $\left(D_{k}\right)$.

A more effective implementation of the 'diagonal function' can be obtained when we consider the set of moduli

$$
\begin{gathered}
\mathrm{vm}_{1}=\mathrm{m}_{1} * \mathrm{~m}_{2 *} \ldots * \mathrm{~m}_{\mathrm{il}}, \quad \mathrm{vm}_{2}= \\
=\mathrm{m}_{\mathrm{ill+1}} * \mathrm{~m}_{\mathrm{ill+2}} \ldots * \mathrm{~m}_{\mathrm{i} 2}, \ldots, \mathrm{vm}_{\mathrm{j}}= \\
=\mathrm{m}_{\mathrm{ij}-1+1} * \mathrm{~m}_{\mathrm{ij}-1+2 *}, \ldots * \mathrm{~m}_{\mathrm{ij}}, \ldots, \mathrm{vm}_{\mathrm{k}}= \\
=\mathrm{m}_{\mathrm{ik}-1+1} * \mathrm{~m}_{\mathrm{ik}-1+2^{*} \ldots . .} * \mathrm{~m}_{\mathrm{ik}}
\end{gathered}
$$

(where $\forall \mathrm{p}, \mathrm{q}=1,2, \ldots, \mathrm{k}$ : $\mathrm{i}_{\mathrm{p}}, \mathrm{i}_{\mathrm{q}}$ integers; $\mathrm{p}<\mathrm{q} \Rightarrow \mathrm{i}_{\mathrm{p}}<\mathrm{i}_{\mathrm{q}}$ and $\mathrm{i}_{\mathrm{k}}=\mathrm{N}$ ) [3]. For the set of moduli $\mathrm{vm}_{1}, \mathrm{vm}_{2}, \ldots, \mathrm{vm}_{\mathrm{k}}$, the 'diagonal function' $\mathbf{D}_{\mathbf{k}}(\cdot)$ is:

$$
\begin{equation*}
D_{k}(X)=\left|\sum_{i=1}^{k} v k_{i} \cdot v x_{i}\right|_{S Q_{k}} \tag{8}
\end{equation*}
$$

where $\left(\mathrm{Vx}_{1}, \mathrm{Vx}_{2}, \ldots, \mathrm{vx}_{\mathrm{k}}\right)$ is the representation of X in the RNS of moduli $\mathrm{vm}_{1}, \mathrm{vm}_{2}, \ldots, \mathrm{vm}_{\mathrm{k}}$, that is

$$
\mathrm{vx}_{\mathrm{i}}=|X|_{v m_{i}}
$$

$$
S Q_{k}=\sum_{j=1}^{k} \frac{M}{v m_{j}}
$$

and

$$
\mathrm{vk}_{\mathrm{j}}=-\left|\frac{1}{v m_{j}}\right|_{S Q_{k}}
$$

Table I compares the different techniques. The MRC is based on a strictly sequential process (eqs.(4)-(5)). It has a time delay $\mathrm{O}(\mathrm{N})$ and its ROM requirement is $\mathrm{O}(\mathrm{N})$ [13]. The implementation of eqs.(2) (6),(7),(8) have a time complexity $\mathrm{O}(\log \mathrm{N})$, since the addition of N values can be performed in parallel using a tree of adders. Since the RNS moduli are pairwise relatively prime, it follows that necessarily $\mathrm{m}_{\mathrm{i}}$ must be greater or equal than $i, \forall \mathrm{i}=1,2, \ldots, \mathrm{~N}$ (for instance we have: $\mathrm{m}_{1} \geq 2, \mathrm{~m}_{2} \geq 3, \mathrm{~m}_{3} \geq 5, \mathrm{~m}_{4} \geq 7$, and so on). Therefore, the total storage ROM is greater than $(1+2+3+\ldots+\mathrm{N})=\Omega\left(\mathrm{N}^{2}\right)$ [5]. Moreover, the 'diagonal function' (D) and its improved implementation $\left(\mathrm{D}_{\mathrm{k}}\right)$ do not support residue-to-binary conversion, whereas the CRT and the $\mathrm{F}_{\mathrm{I}}$ (for $\mathrm{I}=\{\mathrm{i}\}$ and $\mathrm{m}_{\mathrm{i}}=2^{\mathrm{k}}$, k integer) support both magnitude comparison and residue-tobinary conversion.

## V. A Case Study

Table II compares the parallel techniques for the RNS of $\mathrm{N}=4$ moduli $\mathrm{m}_{1}=37, \mathrm{~m}_{2}=41, \mathrm{~m}_{3}=43, \mathrm{~m}_{4}=64$ [9]:

* $\mathrm{L}(l, a)$ denotes a look-up table of $2^{1}$ locations with $a$-bit word length. It has a time delay equal to $\mathrm{t}_{\mathrm{L}}$;
* $\operatorname{MOMA}(\mathrm{N}, a)$ denotes a multi-operand modular adder for N operands with $a$-bit word length. It uses a tree of Carry Save Adders (CSA) and a Ripple Carry Adder (RCA) for final summation [7,9]. It requires ( $\mathrm{N}+1$ ) $a$ full adders (FA) and its time delay is $\mathrm{t}_{\mathrm{MOMA}(\mathrm{N}, \mathrm{A})}=\theta(N) \cdot \mathrm{t}_{\mathrm{FA}}+2 \mathrm{t}_{\mathrm{RCA}(\mathrm{a})}$, where $\theta(N)$ is the minimum number of levels in the CSA tree with N operands (for the case $\mathrm{N}=4$ it results that $\theta(N)=2)$ [7], $\mathrm{t}_{\mathrm{FA}}$ is the time delay of a $\mathrm{FA}, \mathrm{t}_{\mathrm{RCA}(\mathrm{a})}$ is the time delay of a RCA with $a$-bit word length.
* $\mathrm{C}(p)$ denotes a binary comparator with $p$-bit word length. It has a time delay equal to ${ }^{t} C(p)$.

Moreover, let $\Delta$ be the delay of a NAND gate, the following delays are assumed: $\mathrm{t}_{\mathrm{L}}=\Delta, \mathrm{t}_{\mathrm{FA}}=2 \Delta, \mathrm{t}_{\mathrm{RCA}(\mathrm{a})}=\mathrm{a} \cdot \Delta,{ }^{t} C(p)=4 \Delta$ (for $8<p \leq 64$ ) [9]. In Table II, as suggested in [3], the MRC is used in the Preparatory Step (PS) for computing $\mathrm{D}_{\mathrm{k}}(\mathrm{X})$ (in order to obtain the values $\mathrm{vx}_{1}=\left.{ }^{\mid}\right|_{\nu m_{1}}$ and $\mathrm{vx}_{2}={ }^{\prime}|X|_{v m_{2}}$ ). In this case
is included in the set of RNS moduli. The new implementations are superior both to the recent techniques based on the 'diagonal functions', which support magnitude comparison only, and to the Chinese Remainder Theorem (CRT), in terms of time delay and waste of hardware.

Table II: A Case Study

|  |  |  | CRT | D | $\begin{gathered} \mathbf{D}_{\mathbf{k}} \\ \left(\text { for } \mathrm{vm}_{1}=\mathrm{m}_{1} * \mathrm{~m}_{4}=2368\right. \\ \mathrm{vm}_{2}=\mathrm{m}_{2} * \mathrm{~m}_{3}=1763 \text { ) } \\ \hline \end{gathered}$ | $\mathbf{F}_{\mathbf{I}}$ (for $\mathrm{I}=\{4\}$ ) |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | $\begin{gathered} \mathrm{M}=4174784 \\ \left(a=\left\lceil\log _{2} \mathrm{M}\right\rceil=22 \mathrm{bit}\right) \end{gathered}$ | $\begin{gathered} \mathrm{SQ}=376975 \\ \left(a==\log _{2} \mathrm{SQ} 7=19 \mathrm{bit}\right) \end{gathered}$ | $\begin{gathered} \mathrm{SQ}_{2}=4131 \\ \left(a=\left\lceil\log _{2} \mathrm{SQ}_{\mathrm{k}}\right\rceil=13 \mathrm{bit}\right) \end{gathered}$ | $\begin{gathered} \mathrm{M}_{\mathrm{I}}=65231 \\ \left(a=\left\lceil\log _{2} \mathrm{M}_{\mathrm{I}}\right\rceil=16 \text { bit }\right) \end{gathered}$ |
|  | PS - Delay |  | - | - | $\mathrm{t}_{\text {MRC }}$ | - |
|  | ROM |  | $4 \mathrm{~L}(6,22)$ | $4 \mathrm{~L}(6,19)$ | $4 \mathrm{~L}(6,13)$ | $4 \mathrm{~L}(6,16)$ |
|  | $\begin{aligned} & M \\ & O \\ & M \\ & A \end{aligned}$ | $F A$ | $(\mathrm{N}+1) \cdot 22=5 * 22=110$ | $(\mathrm{N}+1) \cdot 19=5 * 19=90$ | $(\mathrm{N}+1) \cdot 22=5 * 13=65$ | $(\mathrm{N}+1) \cdot 16=80$ |
|  |  | Delay | $\begin{gathered} \theta(\mathrm{N}) \cdot \mathrm{t}_{\mathrm{FA}}+2 \mathrm{t}_{\mathrm{RCA}}([\log 2 \mathrm{M}\rceil)= \\ =2 \cdot 2 \Delta+2 \cdot 22 \Delta=48 \Delta \end{gathered}$ | $\begin{gathered} \theta(\mathrm{N}) \cdot \mathrm{t}_{\mathrm{FA}}+2 \mathrm{t}_{\mathrm{RCA}(\log 2 \mathrm{SQ} 7)=}= \\ =2 \cdot 2 \Delta+2 \cdot 19 \Delta=42 \Delta \end{gathered}$ | $\begin{gathered} \theta(\mathrm{N}) \cdot \mathrm{t}_{\mathrm{FA}}+2 \mathrm{t}_{\mathrm{RCA}(\log 2 \mathrm{SQk}])=}=2 \cdot 2 \Delta+2 \cdot 13 \Delta=30 \Delta \end{gathered}$ | $\begin{aligned} & \theta(\mathrm{N}) \cdot \mathrm{t}_{\mathrm{FA}}+2 \mathrm{t}_{\mathrm{RCA}([\log 2 \mathrm{MII}])=}=2 \cdot 2 \Delta+2 \cdot 16 \Delta=36 \Delta \end{aligned}$ |
|  | Extra hardware |  | --- | Not Supported <br> Not Supported |  | --- |
|  | Delay |  | $\begin{gathered} \mathrm{t}_{\mathrm{L}}+\mathrm{t}_{\mathrm{MOMA}(\mathrm{~N},\lceil\log 2 \mathrm{M}\rceil)}= \\ =\Delta+48 \Delta=49 \Delta \end{gathered}$ |  |  | $\begin{gathered} \mathrm{t}_{\mathrm{L}}+\mathrm{t}_{\text {MOMA }(\mathrm{N},\lceil\log 2 \mathrm{MI}\rceil)}= \\ =\Delta+36 \Delta=37 \Delta \end{gathered}$ |
|  |  |  | C(22) | $\mathrm{C}(19+6)$ | $\mathrm{C}(13+6+6)$ | C(16+6) |
|  | Delay |  | $\begin{gathered} 2 *\left(\mathrm{t}_{\mathrm{L}}+\mathrm{t}_{\mathrm{MOMA}(\mathrm{~N},}\right. \\ \lceil\log 2 \mathrm{M} 7)+\mathrm{t}_{\mathrm{c}([\log 2 \mathrm{M}\rceil)}= \\ =2 *(\Delta+48 \Delta)+4 \Delta=102 \Delta \end{gathered}$ | $\begin{gathered} 2^{*}\left(\mathrm{t}_{\mathrm{L}}+\mathrm{t}_{\mathrm{MOMA}(\mathrm{~N},\lceil\log 2 \mathrm{SQ} 7)}\right)_{+} \\ \mathrm{t}_{\mathrm{c}([\log 2 \mathrm{SQ} 7)=}= \\ =2 *(\Delta+42 \Delta)+4 \Delta=90 \Delta \end{gathered}$ | $\begin{gathered} 2 *\left(\mathrm{t}_{\mathrm{MRC}}+\mathrm{t}_{\mathrm{L}}+\mathrm{t}_{\mathrm{MOMA}(\mathrm{~N},}\right. \\ \lceil\log 2 \mathrm{SQk}\rceil)+\mathrm{t}_{\mathrm{c}(\lceil\log 2 \mathrm{SQk}\rceil\rceil}= \\ 2 *(2 \Delta+\Delta+30 \Delta)+4 \Delta=70 \\ \Delta \end{gathered}$ | $\begin{gathered} 2 *\left(\mathrm{t}_{\mathrm{L}}+\mathrm{t}_{\mathrm{MOMA}(\mathrm{~N},}\right. \\ \lceil\log 2 \mathrm{MI}))+\mathrm{t}_{\mathrm{c}(\sqrt{\log 2 \mathrm{MI}\rceil)}=}= \\ =2 *(\Delta+36 \Delta)+4 \Delta=78 \Delta \end{gathered}$ |

the PS has a time delay of $\mathrm{t}_{\mathrm{MRC}}=2 \Delta$ and it does not require extra hardware.

From Table II it results that the new approach is superior to the approaches based on the 'diagonal function' since they support magnitude comparison only. The new approach is also superior to the CRT in terms of time delay (a $24 \%$ save for residue-to-binary conversion, a $23 \%$ save for magnitude comparison) and waste of hardware (a $27 \%$ save for FA and ROM).

Finally, we remark that unlike other techniques that provide approximate methods for non-modular operation $[6,8]$, the new functions support exact methods for residue-to-binary conversion and magnitude comparison without imposing severe constraints on the set of moduli, as other approaches [10,11,14].

## VI. CONCLUSION

This paper presents a new class of monotone functions defined from the RNS to the integers - that support parallel implementations of residue-to-binary conversion and magnitude comparison, if a modulus of the kind $2^{\mathrm{k}}$ ( k integer)

## APPENDIX

A) $\quad$ Since $I \cup J=\{1,2, \ldots, N\}$, let

$$
\begin{aligned}
& P_{I}=\prod_{i \in I} m_{i} \\
& P_{J}=\prod_{j \in J} m_{j}
\end{aligned}
$$

then

$$
P_{I} \cdot P_{J}=M
$$

Therefore, from the CRT it follows that an integer K exists so that [13]:
$\equiv$

$$
\begin{array}{r}
\left|\left(-1+\sum_{j \in J} M_{j} \cdot\left|\frac{1}{M_{j}}\right|_{m_{j}}\right)\right|_{M_{I}} . \\
\left|K \cdot P_{J}\right|_{M_{I}} \\
\equiv \\
\left.\left.|K \cdot M \cdot| \frac{1}{P_{I}}\right|_{M_{I}}\right|_{M_{I}} .
\end{array}
$$

## Hence

$$
\begin{aligned}
\left||X|_{M} \cdot S_{I N V} \cdot\left(-1+\sum_{j \in J} M_{j} \cdot\left|\frac{1}{M_{j}}\right|_{m_{j}}\right)\right|_{M_{I}} & \equiv \\
\left.\left.\left||X|_{M} \cdot K \cdot S_{I N V} \cdot M \cdot\right| \frac{1}{P_{I}}\right|_{M_{I}}\right|_{M_{I}} & \equiv \\
\left.\left.\left||X|_{M} \cdot K \cdot\right| \frac{1}{P_{I}}\right|_{M_{I}} \cdot M_{I}\right|_{M_{I}} & \equiv 0 .
\end{aligned}
$$

B) From

$$
\left|M \cdot S_{I N V}\right|_{M_{I}} \equiv\left|M_{I}\right|_{M_{I}} \quad=0
$$

it results

$$
\begin{aligned}
& \left.\left|\sum_{j \in J} M \cdot S_{I N V} \cdot\right| \frac{1}{M_{j}}\right|_{m_{j}} \cdot\left[\left.\frac{|X|_{M}}{m_{j}}\right|_{M_{I}} \equiv\right. \\
& \left.\left.\left|M_{I} \cdot \sum_{j \in J}\right| \frac{1}{M_{j}}\right|_{m_{j}} \cdot\left[\frac{|X|_{M}}{m_{j}}\right]\right|_{M_{I}} \equiv
\end{aligned}
$$

## REFERENCES

[1] A.A. Albert, Fundamental Concepts of Higher Algebra, University of Chicago Press, Chicago, 1956.
[2] G. Dimauro, S. Impedovo and G. Pirlo, "A new technique for fast numbers comparison in the Residue Number System", IEEE-Transaction on Computers, Vol. 42, No. 5, May 1993, pp. 608612.
[3] G.Dimauro, S. Impedovo, G. Pirlo, and A. Salzo, "RNS architectures for the implementation of the 'diagonal function', Information Processing Letters, vol. 73, 2000, pp.189-198.
[4] G. Dimauro, S. Impedovo, R. Modugno, G. Pirlo, R. Stefanelli, "Residue-to-Binary Conversion by the Quotient Function", IEEE Trans. Circuits Syst - Part II, Vol.50, No.8, Aug. 2003, pp. 488-493.
[5] K.M. Elleithy, M.A. Bayoumi, "Fast and flexible architectures for RNS arithmetic decoding", IEEE Trans. Circuits Syst. - II, Vol. 39, Apr. 1992, pp. 226-235.
[6] C.Y.Hung and B. Parhami, "An approximate sign detection method for residue number systems", Computers Math. Applic., Vol. 10, No. 4/5, , 1984, pp. 331-342.
[7] K.Hwang, Computer Arithmetic: Principle,Architecture, Design, NewYork: Wiley, 1979.
[8] J.Y.Kim, K. H. Park, H. S. Lee, "Efficient Residue-to-Binary Conversion Technique with Rounding Error Compensation" , IEEE Trans. Circuits Syst., Vol. CAS-38, n. 3, March 1991, pp. 315-317.
[9] S. J. Piestrak, "Design of Residue Generators and Multi-operand Modular Adders Using Carry-Save Adders", IEEE-Transaction on Computers, Vol. 43, No. 1, Jan. 1994, pp. 68-77.
[10] F. Pourbigharaz, H.M. Yassine, "A Signed-Digit Architecture for residue to Binary Transformation", IEEE-Transaction on Computer, . Vol. 46, N. 10, Oct. 1997, pp. 1146-1150.
[11] A.B. Premkumar, "An RNS to Binary Converter in a Three Moduli Set with Common Factors", IEEE Trans. Circuits Syst - Part II, Vol. 42, N. 4, April 1995, pp. 298-301.
[12] S.Szabó and R. I. Tanaka, Residue Arithmetic and its Applications to Computer Technology, New York: McGraw-Hill, 1967.
[13] F.J.Taylor, "Residue arithmetic: a tutorial with examples", Computer, pp.50-62, vol.17, no. 5, May 1984.
[14] W.Wang, M.N.S.Swamy, M.O.Ahmad, Y.Wang, "A High-Speed Residue-to-Binary Converter for Three-Moduli ( $2^{\mathrm{k}}, 2^{\mathrm{k}-1}, 2^{\mathrm{k}-1}-1$ ) RNS and a Scheme for Its VLSI Implementation", IEEE Trans. Circuits Syst - Part II, Vol.47, No.1, Dec.2000, pp. 1576-1581.

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name. It lists military and work experience, including summer and fellowship jobs. Job titles are capitalized. The current job must have a location; previous positions may be listed without one. Information concerning previous publications may be included. Try not to list more than three books or published articles. The format for listing publishers of a book within the biography is: title of book (city, state: publisher name, year) similar to a reference. Current and previous research interests ends the paragraph.


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