Two-Stage Optimum Design Method For Surface Acoustic Wave Duplexers Using Differential Evolution Algorithms

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Abstract—This paper proposes a novel computer-aided design method to decide on optimal structures of Surface Acoustic Wave (SAW) duplexers, which are used widely in the radio circuits of mobile communication systems. By using the proposed design method, engineers’ knowledge can be easily reflected in the structure of SAW duplexer. The proposed design method is based on the computer simulation of SAW duplexer and composed of two stages: exploration and exploitation. In the first or exploration stage, the structural design of a target SAW duplexer is formulated as a constraint satisfaction problem. Then, by using an extended version of a recently-developed evolutionary algorithm, namely Differential Evolution (DE), various feasible solutions are obtained for the constraint satisfaction problem. In the second or exploitation stage, for deciding on an optimal structure of the SAW duplexer by considering the feasible solutions obtained in the first stage and the knowledge of engineers, the structural design of the SAW duplexer is reformulated again as a constrained optimization problem. Beside, for solving the constrained optimization problem effectively, another version of DE is contrived. The usefulness of the two-stage design method is also demonstrated through the structural design of a practical SAW duplexer that consists of four SAW resonators and two SAW filters.

Keywords—Computer-aided design, constrained optimization, evolutionary computation, optimum design, SAW duplexer.

I. INTRODUCTION

SURFACE Acoustic Wave (SAW) is an acoustic wave traveling along the surface of a material exhibiting elasticity. SAW devices are mechanical and electrical devices that use the acoustic wave in electronic components to provide a number of different functions including delay lines, resonators, filters, and so on [1]–[4]. Recently, SAW duplexers are used widely in the radio circuits of mobile communication systems such as cellular phones, smart phones, and Personal Digital Assistants (PADs). Incidentally, the duplexer is a key device for the radio circuits of various communication systems that isolates a receiver (Rx) from a transmitter (Tx) while permitting them to share a common antenna (ANT). Furthermore, SAW duplexers can provide small, thin, rugged, and cost competitive duplexers with outstanding frequency response characteristics [5]–[8].

SAW devices have one or more specific electrodes, which are called Inter-Digital Transducers (IDTs), to convert acoustic waves to electrical signals and vice versa by exploiting the piezoelectric effect of certain materials. Besides, SAW devices have several reflectors to restrain the spread of acoustic waves. Therefore, the frequency response characteristics of a SAW device is governed primarily by its geometrical structure, namely the configurations of IDTs and reflectors fabricated on a piezoelectric substrate. By deciding on an appropriate structure of the SAW device, we can carry out a desirable function.

In order to avoid the repetition of the design by trial and error, the structural design of a SAW device is often formulated as an optimization problem. After that, for solving the optimization problem, the optimum design method that combines an optimization algorithm with the simulator of the SAW device is used [9]–[14]. Author has also reported such an optimization design method for Double Mode SAW (DMS) filters by using a recently-developed evolutionary algorithm, namely Differential Evolution (DE) [14]. Incidentally, DE is arguably one of the most powerful optimization algorithms in current use. Because of its simple but powerful searching capability, DE has been used in several scientific and engineering applications [15]–[18]. The conventional optimum design method of DMS filters could reduce the cost and time spent for their developments. However, unlike DMS filters, not only the structure but also the function of the SAW duplexer is so complex. Therefore, we can’t easily formulate the structural design of the SAW duplexer as an optimization problem. In other words, it is very hard to define directly the objective function and the design space for predicting and improving the quality of the SAW duplexer in terms of performance, robustness, and yield.

In this paper, a novel computer-aided design method of SAW duplexers is proposed. The proposed design method consists of two stages, namely exploration and exploitation. By using the proposed two-stage design method, engineers’ knowledge can be easily reflected in the structure of SAW duplexer. As stated above, SAW duplexers usually have complex structures. For example, SAW duplexer to be designed in this paper consists of two DMS filters and four SAW resonators fabricated on a piezoelectric substrate. Therefore, in the structural design of the SAW duplexer, a number of design parameters have to be considered. Furthermore, the SAW duplexer must be designed for operation in frequency bands used by Rx and Tx, and must provide the frequency separation between Rx and Tx. In the first or exploration stage, the structural design of a SAW duplexer is
formulated as a constraint satisfaction problem instead of an optimization problem. The solutions that satisfy all constraints of the problem are called feasible ones. In order to obtain various feasible solutions at one time, we propose a new DE-based algorithm called DE for Constraint Satisfaction (DECS). DECS has an archive to store all feasible solutions found so far. Through the analysis of the feasible solutions obtained by DECS, we can estimate the feasible region in the design space of the SAW duplexer.

In the second or exploitation stage, for choosing the most desirable solution within the feasible region estimated in the first stage, the structural design of the above SAW duplexer is reformulated again as a constrained optimization problem. In addition to the information about the SAW duplexer obtained in the first stage, the knowledge, wisdom, and preference of engineers can be used to define the constrained optimization problem. For solving the constrained optimization problem effectively, another DE-based algorithm, which is called DE for Constrained Optimization (DECO), is also proposed.

The rest of this paper is organized as follows. Section II explains the structure of SAW duplexer. Besides, the network model of SAW duplexer is derived. Section III formulates the structural design of SAW duplexer as a constraint satisfaction problem. In order to obtain various feasible solutions for the constraint satisfaction problem, a new DE-based algorithm, or DECS, is proposed in Section IV. Section V summarizes the first stage of the proposed two-stage design method. Section VI provides the second stage of the two-stage design method. In the second stage, considering the outcome of the first stage in addition to engineers’ knowledge, the structural design of SAW duplexer is reformulated again as a constrained optimization problem. Furthermore, another DE-based algorithm, or DECO, is proposed to solve the constrained optimization problem. Through the structural design of a practical SAW duplexer, the usefulness of the two-stage design method is also demonstrated in Section VI. Finally, Section VII concludes the paper.

II. SAW DUPLEXER

A. Structure of SAW Duplexer

Fig. 1 illustrates two elemental components of various SAW devices, namely Inter-Digital Transducers (IDT) and Short Metal Strip Array (SMSA). IDT shown in Fig. 1(a) is assembled from two comb-shaped facing electrodes, which are referred to as fingers and used to excite and detect acoustic waves. On the other hand, SMSA shown in Fig. 1(b) is assembled from a number of metal strips and used to reflect acoustic waves.

Fig. 2 shows the basic structure of SAW duplexer that consists of four SAW resonators and two DMS filters. Each of SAW resonators consists of three elemental components: one IDT sandwiched between two SMSAs. On the other hand, each of DMS filters consists of five elemental components: three IDTs sandwiched between two SMSAs. All the parts of the SAW duplexer in Fig 2, namely four SAW resonators and two DMS filters, are fabricated on a piezoelectric substrate.

A pair of port-1 and port-2 of the SAW duplexer in Fig. 2 is connected to a common ANT and works as a balanced port for operating a differential mode signal [19]. On the other hand, port-3 and port-4 are connected to Tx and Rx, respectively, and work as normal ports for operating single mode signals. The two DMS filters connected to port-3 and port-4 are band-pass filters that provide two separate pass-bands for Tx and Rx.

B. Network Model of SAW Duplexer

In order to evaluate the frequency response characteristics of SAW duplexer through the computer simulation, we derive a network model of SAW duplexer. First of all, the behavior of IDT can be analyzed by using a three-port equivalent circuit model shown in Fig. 3: port-a and port-b are acoustic ports, while port-c is an electric port. In the equivalent circuit model of IDT, the values of impedances $Z_1$ and $Z_2$, admittance $Y_m$, and capacitance $C_T$ depend on the size of IDT such as the number and the length of fingers [20]. Besides, a two-port equivalent circuit model of SMSA can be also derived by shorting the electric port (port-c) of the equivalent circuit model of IDT.

The SAW duplexer shown in Fig. 2 is composed of a lot of elemental components, namely IDTs and SMSAs. Because all
the elemental components of the SAW duplexer are connected acoustically in cascade on a piezoelectric substrate, a whole circuit model of the SAW duplexer can be made up from the equivalent circuit models of its elemental components in the same way with DMS filters [14]. Furthermore, by terminating the respective ports with characteristic impedances, we can transform the whole circuit model of the SAW duplexer into the network model represented by a scattering matrix $S$ as

$$
\begin{bmatrix}
    a_1 \\
    a_2 \\
    a_3 \\
    a_4
\end{bmatrix} =
\begin{bmatrix}
    b_1 \\
    b_2 \\
    b_3 \\
    b_4
\end{bmatrix} =
S
\begin{bmatrix}
    s_{11} & s_{12} & s_{13} & s_{14} \\
    s_{21} & s_{22} & s_{23} & s_{24} \\
    s_{31} & s_{32} & s_{33} & s_{34} \\
    s_{41} & s_{42} & s_{43} & s_{44}
\end{bmatrix}
\begin{bmatrix}
    a_1 \\
    a_2 \\
    a_3 \\
    a_4
\end{bmatrix}
$$

(1)

where $a_n$ and $b_n$ (n=1, 2, 3, 4) denote the input- and output-signals of the port-n of the SAW duplexer in Fig. 2. Besides, each $s$-parameter $s_{mn}$ in the scattering matrix $S$ provides the transmission characteristic from port-m to port-n.

As stated above, a pair of port-1 and port-2 of the SAW duplexer works as a balanced port. In order to evaluate the frequency response characteristics of the SAW duplexer strictly, differential mode signals need to be segregated from common mode signals at the balanced port. Therefore, according to the balanced network theory [21], the input- and output-signals $a_n$ and $b_n$ (n=1, 2) of the balanced port are converted into the input- and output-signals $a_d$ and $b_d$ of differential mode as

$$
\begin{align*}
    a_d &= \frac{1}{\sqrt{2}}(a_1 - a_2), \\
    b_d &= \frac{1}{\sqrt{2}}(b_1 - b_2).
\end{align*}
$$

(2)

Similarly, the input- and output-signals $a_n$ and $b_n$ (n=1, 2) of the balanced port of the SAW duplexer are also converted into the input- and output-signals $a_c$ and $b_c$ of common mode as

$$
\begin{align*}
    a_c &= \frac{1}{\sqrt{2}}(a_1 + a_2), \\
    b_c &= \frac{1}{\sqrt{2}}(b_1 + b_2).
\end{align*}
$$

(3)

From (2) and (3), the network model in (1) is transformed into another form with the mix-mode $s$-parameters as

$$
\begin{bmatrix}
    b_d \\
    b_c \\
    b_3 \\
    b_4
\end{bmatrix} = T S T^{-1}
\begin{bmatrix}
    a_d \\
    a_c \\
    a_3 \\
    a_4
\end{bmatrix} =
\begin{bmatrix}
    s_{dd} & s_{dc} & s_{d3} & s_{d4} \\
    s_{cd} & s_{cc} & s_{c3} & s_{c4} \\
    s_{3d} & s_{3c} & s_{33} & s_{34} \\
    s_{4d} & s_{4c} & s_{43} & s_{44}
\end{bmatrix}
\begin{bmatrix}
    a_d \\
    a_c \\
    a_3 \\
    a_4
\end{bmatrix},
$$

(4)

where the matrix $T$ is described as

$$
T = \frac{1}{\sqrt{2}}
\begin{bmatrix}
    1 & 1 & 0 & 0 \\
    -1 & 1 & 0 & 0 \\
    0 & 0 & \sqrt{2} & 0 \\
    0 & 0 & 0 & \sqrt{2}
\end{bmatrix}.
$$

(5)

III. CONSTRAINT SATISFACTION PROBLEM

Desirable frequency response characteristics of the SAW duplexer can be specified by a set of constraints for them. Thus the structural design of the SAW duplexer is formulated as a constraint satisfaction problem. In order to describe the shapes of the elemental components of the SAW duplexer, we choose $D$ design parameters $\bar{x} = (x_1, \ldots, x_j, \ldots, x_D)$ including the length and the number of IDT’s fingers. The design parameters’ values are limited by the lower $\bar{x}_j$ and the upper $\bar{x}_j$ bounds as

$$
\bar{x}_j \leq x_j \leq \bar{x}_j, \quad j = 1, \ldots, D.
$$

(6)

From the network model in (4), we define three performance criteria to evaluate the frequency response characteristics of the SAW duplexer, which depend on the above design parameter $\bar{x}$ and vary with the frequency. Let $\omega \in \Omega$ be a frequency point sampled from the frequency bands $\Omega$ used by Tx and Rx.

Firstly, from the mix-mode $s$-parameter $s_{dd}$ of $a_d$ to $b_d$ in (4), the transmission characteristic of Tx to ANT is defined as

$$
\Gamma_T(\bar{x}, \omega) = 20 \log_{10}|f_{dd}(\bar{x}, \omega)|.
$$

(7)

Secondly, from the mix-mode $s$-parameter $s_{dc}$ of $a_d$ to $b_c$, the transmission characteristic of ANT to Rx is defined as

$$
\Gamma_A(\bar{x}, \omega) = 20 \log_{10}|f_{dc}(\bar{x}, \omega)|.
$$

(8)

Finally, from the mix-mode $s$-parameter $s_{dd}$ of $a_c$ to $b_d$, the transmission characteristic of Tx to Rx is defined as

$$
\Gamma_T(\bar{x}, \omega) = 20 \log_{10}|f_{dd}(\bar{x}, \omega)|.
$$

(9)

Now, we define some constraints for the above performance criteria. Firstly, let $\Omega_1$ be a set of frequency points $\omega \in \Omega_1$ sampled from the pass-band of Tx. Besides, let $\Omega_2$ be a set of frequency points sampled from the stop-band of Tx. Thereby, two constraints for the performance criterion defined in (7) are given by its lower $L_T(\omega)$ and upper $U_T(\omega)$ bounds as

$$
\begin{align*}
    g_T(\bar{x}, \omega) &= \Gamma_T(\bar{x}, \omega) - L_T(\omega) \leq 0, \quad \omega \in \Omega_1, \\
    g_T(\bar{x}, \omega) &= \Gamma_T(\bar{x}, \omega) - U_T(\omega) \leq 0, \quad \omega \in \Omega_2.
\end{align*}
$$

(10)
Secondly, let $\Omega_3$ and $\Omega_4$ be the sets of frequency points sampled, respectively, from the pass- and stop-bands of Rx. Thereby, two constraints for the performance criterion in (8) are given by its lower $L_\beta(\omega)$ and upper $U_\beta(\omega)$ bounds as

$$
\begin{align*}
g_3(\bar{x}, \omega) & = L_\beta(\omega) - \Gamma_\beta(\bar{x}, \omega) \leq 0, \ \omega \in \Omega_3, \\
g_4(\bar{x}, \omega) & = \Gamma_\beta(\bar{x}, \omega) - U_\beta(\omega) \leq 0, \ \omega \in \Omega_4.
\end{align*}
$$

Finally, let $\Omega_4$ be a set of frequency points sampled from both bands of Tx and Rx. Then a constraint for the performance criterion in (9) is given by its upper $U_I(\omega)$ bound as

$$
g_5(\bar{x}, \omega) = \Gamma_I(\bar{x}, \omega) - U_I(\omega) < 0, \ \omega \in \Omega_1.
$$

Let $X$ be a set of the design parameters $\bar{x} = (x_1, \cdots, x_D)$ that satisfy the boundary constraints in (6). From now on, the set $X$ is referred to as the design space of the SAW duplexer. For the constraint satisfaction problem, we look for a number of solutions $\bar{x} \in X$ that satisfy all inequality constraints in (10), (11), and (12). As stated above, the solutions $\bar{x} \in X$ that satisfy all constraints are called feasible solutions. Even though the existence of feasible solution is not guaranteed theoretically, a number of feasible solutions usually exist for the constraint satisfaction problem. Therefore, we define the feasible region $F$ ($F \subseteq X$) as a whole set of feasible solutions $\bar{x} \in F \subseteq X$.

IV. ALGORITHM FOR CONSTRAINT SATISFACTION

A. Representation

In order to obtain a number of feasible solutions $\bar{x} \in F \subseteq X$ for the constraint satisfaction problem at one time, we employ a new DE-based algorithm called DECS [22]. A purpose of the first stage of the design method is to estimate the feasible region $F \subseteq X$ from the set of feasible solutions obtained by DECS. Like the canonical DE [15], DECS has $N_P$ tentative solutions, which are referred to as individuals, in the population $P$. The $i$-th individual $\bar{x}_i \in P$ ($i = 1, \cdots, N_P$) of the population $P$ is represented by a $D$-dimensional real vector as

$$
\bar{x}_i = (x_{ij}, \cdots, x_{i,j}, \cdots, x_{iD}),
$$

where $x_{ij} \in R$ and $x_{ij} \leq x_{ij} \leq x_{ij}$, $j = 1, \cdots, D$.

Let $\text{rand}(0, 1)$ be the random number generator that returns a uniformly distributed random number in the range $[0, 1]$. Thereby, the initial population is generated randomly as

$$
x_{ij} = \text{rand}(0, 1)(\bar{x}_{ij} - \bar{x}_{ij}) + \bar{x}_{ij},
$$

where $j = 1, \cdots, D$ and $i = 1, \cdots, N_P$.

B. DE Strategy

DECS uses the strategy of DE to generate a new individual. Even though various strategies have been proposed for DE [16], we employ a basic strategy named “DE/rand/1/bin” [15]. In the basic strategy, each of the individual $\bar{x}_i \in P$ ($i = 1, \cdots, N_P$) is assigned to “the target vector” in turn. Except for the target vector, three mutually different individuals, say $\bar{x}_i, \bar{x}_{i+1}, \bar{x}_{i+2}$, and $\bar{x}_i$ ($i \neq i-1 \neq i-2 \neq i+3$), are selected randomly from $P$. After that, a new individual $\bar{u} = (u_1, \cdots, u_j, \cdots, u_D)$ in $R^D$ called “the trial vector” is generated from the four individuals as

$$
u_j =
\begin{cases}
x_{j,\text{rand}} + S_F (x_{j,\text{rand}} - x_{i,\text{rand}}), & \text{if } \text{rand}[0, 1] \leq C_R, \\
x_{j,\text{rand}}, & \text{otherwise},
\end{cases}
$$

where the scale factor $S_F \in R$, $(0 < S_F < 2)$ and the crossover rate $C_R \in [0, 1]$ are control parameters given by the user.

If the strategy in (15) makes an element $u_j$ of the trial vector $\bar{u}$ outside the range $[0, 1]$, it is returned to the range as

$$
u_j =
\begin{cases}
x_{j,\text{rand}} + \text{rand}[0, 1](x_{j,\text{rand}} - x_{i,\text{rand}}), & \text{if } u_j < x_{j,\text{rand}}, \\
x_{j,\text{rand}} + \text{rand}[0, 1](x_{j,\text{rand}} - x_{i,\text{rand}}), & \text{if } u_j > x_{j,\text{rand}}.
\end{cases}
$$

A design parameter $x_j (j = 1, \cdots, D)$ in (6) takes either a continuous value $x_j \in R$ or a discrete value $x_j \in Q$ spaced apart by an interval $e_j \in R$, where $Q$ is the set of discrete design parameters. However, every element $x_{j,\text{rand}} \in R$ of the individual $\bar{x}_i \in P$ takes a real value. Therefore, elements $x_{j,i} \in R$ of the individual $\bar{x}_i \in P$ corresponding to discrete design parameters $x_j \in Q$ are converted, respectively, into $x_j \in Q$ as

$$
x_j = \text{round}
\left(
\frac{x_{j,\text{rand}}}{e_j}
\right)
\times e_j
$$

where $\text{round}(r)$ rounds a real $r \in R$ to the nearest integer.

Now, we have to measure the goodness of the individual $\bar{x}_i \in P$ that corresponds to a solution $\bar{x} \in X$. Therefore, from the five constraints shown in (10), (11), and (12), we define the penalty function $g(\bar{x})$ for $\bar{x}_i \in P$ to be minimized as

$$
g(\bar{x}_i) = \sum_{p=1}^{D} \sum_{\omega \in \Omega_p} \max\left\{ g_p(\bar{x}_i, \omega), 0 \right\}
$$

where $g(\bar{x}_i) = 0$ holds for a feasible solution $\bar{x} \in F$, while $g(\bar{x}_i) > 0$ holds for an infeasible solution $\bar{x} \notin F$. 
C. DECS Algorithm

DECS is an extended version of Dispersive DE (DDE) proposed by authors [23]. Therefore, DECS is based on the steady-state model that uses only one population \( P \). First of all, the initial population \( P \) is generated randomly as shown in (4). DECS generates more than one trial vector \( \tilde{u} \) from each target vector \( \tilde{x}_i \in P \). If one of the newborn trial vectors \( \tilde{u} \) is not worse than the target vector \( \tilde{x}_i \in P \), the target vector \( \tilde{x}_i \in P \) is replaced by the new excellent trial vector \( \tilde{u} \) immediately. In order to obtain a number of feasible solutions, DECS has an archive \( A \) to store all feasible solutions found so far. If a newborn trial vector \( \tilde{u} \) corresponds to a feasible solution \( \tilde{x} \in F \), it is added to the archive \( A \). The control parameters of DECS are the maximum number of all the penalty function evaluation \( N_p \), the population size \( N_p \), the maximum number of the trial vectors generated from one target vector \( N_t \), and two control parameters \( \gamma_s \) and \( C_R \) used by the basic strategy in (15). Thereby, the DECS algorithm works as follows:

[DECS Algorithm]

1) Initialize the archive such as \( A = \phi \).
2) Randomly generate \( \tilde{x}_i \in P \) where \( i = 1, \cdots, N_p \).
3) For all \( \tilde{x}_i \in P \), evaluate \( g(\tilde{x}_i) \). Let \( k = N_p \).
4) If \( k > N_p \) holds then output all feasible solutions \( \tilde{x}_i \in A \).
5) For each \( \tilde{x}_i \in P \), repeat \( N_t \) times from Steps 6 to 10.
6) Randomly select \( \tilde{x}_{i1}, \tilde{x}_{i2}, \) and \( \tilde{x}_{i3} \) from \( P \).
7) Generate a trial vector \( \tilde{u} \) by (15) and (16).
8) Evaluate \( g(\tilde{u}) \). Let \( k = k + 1 \).
9) If \( g(\tilde{u}) \leq g(\tilde{x}_i) \) holds then replace \( \tilde{x}_i \in P \) by \( \tilde{u} \) such as \( \tilde{x}_i = \tilde{u} \) and \( g(\tilde{x}_i) = g(\tilde{u}) \).
10) If \( g(\tilde{u}) = 0 \) holds then \( A = A \cup \{\tilde{u}\} \).
11) return to Step 4.

V. FIRST STAGE OF DESIGN METHOD

A. Setup of Computational Experiment

The simulator of the SAW duplexer based on the network model in (4) was made by MATLAB and converted into the library used by the Java language. The specifications for the SAW duplexer, namely the upper and lower bounds of the performance criteria, were specified at 301 frequency points \( \omega \in \Omega \) within the range: \( 700 \sim 1000[\text{MHz}] \). In order to describe the structure of the SAW duplexer, we selected \( D = 18 \) design parameters with their upper and lower bounds. The design space of the SAW duplexer was given exactly by Table I.

The proposed DECS was coded by Java and executed on a commodity computer (CPU: Intel Core i7; 3.33[GHz]; OS: Windows XP). The above library of the SAW duplexer’s simulator was included in the program of DECS. From a prior outcome of an experiment, the control parameters of DECS were chosen as \( N_p = 10000, N_t = 100, N_T = 5, \gamma_s = 0.5, \) and \( C_R = 0.9 \). Thereby, in order to a set of obtain feasible solutions, DECS was applied to the constraint satisfaction problem.

Table I Design space of SAW duplexer

<table>
<thead>
<tr>
<th>( x_j )</th>
<th>( [x_{j1}, x_{j2}] )</th>
<th>( x_j )</th>
<th>( [x_{j1}, x_{j2}] )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( x_1 )</td>
<td>[250, 350]</td>
<td>( x_{10} )</td>
<td>[45, 65]</td>
</tr>
<tr>
<td>( x_2 )</td>
<td>[3900, 4030]</td>
<td>( x_{11} )</td>
<td>[2.20, 2.40]</td>
</tr>
<tr>
<td>( x_3 )</td>
<td>[16.5, 24.5]</td>
<td>( x_{12} )</td>
<td>[16.5, 24.5]</td>
</tr>
<tr>
<td>( x_4 )</td>
<td>[12, 20]</td>
<td>( x_{13} )</td>
<td>[12, 20]</td>
</tr>
<tr>
<td>( x_5 )</td>
<td>[35, 65]</td>
<td>( x_{14} )</td>
<td>[45, 65]</td>
</tr>
<tr>
<td>( x_6 )</td>
<td>[2.26, 2.46]</td>
<td>( x_{15} )</td>
<td>[2.22, 2.32]</td>
</tr>
<tr>
<td>( x_7 )</td>
<td>[250, 350]</td>
<td>( x_{16} )</td>
<td>[55, 95]</td>
</tr>
<tr>
<td>( x_8 )</td>
<td>[3900, 4030]</td>
<td>( x_{17} )</td>
<td>[35, 65]</td>
</tr>
<tr>
<td>( x_9 )</td>
<td>[55, 95]</td>
<td>( x_{18} )</td>
<td>[2.00, 2.20]</td>
</tr>
</tbody>
</table>

B. Result of Computational Experiment

By only one trial of DECS, we could obtain \(|A|=5453\) feasible solutions \( \tilde{x} \in A \) of the constraint satisfaction problem. Fig. 4 shows the range of the design parameters \( x_j (j=1,\cdots,D) \) included in the feasible solutions \( \tilde{x} \in A \subseteq F \), where the respective design parameters’ values are normalized between 0 and 1. The maximum and minimum values of the design parameters \( x_j (j=1,\cdots,D) \) included in \( \tilde{x} \in A \subseteq F \) are plotted by broken lines in Fig. 4. The center values of those design parameters are also plotted by a solid line. From Fig. 4, we get a rough image of the feasible region \( F \subseteq X \). For example, the available value of a sensitive design parameter is restricted within a narrow interval. Besides, several design parameters have no influence upon the performance of the SAW duplexer.

The frequency response characteristics of the SAW duplexer achieved by one of the feasible solutions \( \tilde{x} \in A \) are shown in Figs. 5 and 6. Fig. 5 shows the pass-band characteristics of Tx to ANT and ANT to Rx. The upper and lower bounds for them are described by broken lines in Fig. 5 too. Fig. 6 shows the transmission characteristics of Tx to Rx, which is the isolation characteristic between Tx and Rx. The upper bound of the isolation characteristic is also shown by broken line in Fig. 6.
From Figs. 5 and 6, we can confirm that the feasible solution $\hat{x} \in A$ satisfies all constraints shown in (10), (11), and (12).

Under the assumption that the feasible region $F$ consists of only one island that is simply connected and convex, a solution $\bar{z} = (z_1, \cdots, z_J)$ composed by the center values $z_j$ of the respective design parameters $x_j$ in Fig. 4 exists at the center of the feasible region $F$. The literature [24] asserts that such a solution $\bar{z}$ is also feasible and gives a robust solution of the design problem against the influence of uncertain factors.

The frequency response characteristics of the SAW duplexer achieved by the center solution $\bar{z}$ are shown in Figs. 7 and 8. Fig. 7 shows the band-pass characteristics of Tx to ANT and ANT to Rx with their upper and lower bounds. Fig. 8 shows the isolation characteristics with its upper bound. In Figs. 7 and 8, several upper and lower bounds have been violated. Thus the center solution $\bar{z} \in X$ in Fig. 4 is infeasible. Consequently, the feasible region $F \subseteq X$ may not be convex. Otherwise, there is an interaction between several design parameters.

VI. SECOND STAGE OF DESIGN METHOD

A. Constrained Optimization Problem

In order to decide on an optimum structure of the SAW duplexer considering the result of the first stage, the structural design of the SAW duplexer is reformulated as a constrained optimization problem. First of all, by considering the feasible region $F \subseteq X$ which can be estimated from Fig. 4, we reduce the SAW duplexer’s design space given by Table I. Exactly, we narrow the search range $[x_{j,\min}, x_{j,\max}]$ of several design parameters in Table I. Furthermore, we fix the values of several design parameters. As a result, we have only to decide on $D$ design parameters $\tilde{x} = (x_1, \cdots, x_D)$ in the reduced design space of the SAW duplexer $\tilde{X} \subseteq X$. The isolation characteristic of the SAW duplexer in (9) is chosen for the objective function to be minimized. Consequently, from (9), (10), (11), and (12), the constrained optimization problem is formulated as

$$\begin{align*}
\text{minimize} & \quad f(\tilde{x}) = \sum_{i=1}^{P} \Gamma_i(\tilde{x}, \omega), \\
\text{subject to} & \quad g_{p}(\tilde{x}, \omega) \leq 0, \omega \in \Omega_p, (p = 1, \cdots, 5), \\
& \quad \tilde{x} \in \tilde{X}. \quad (19)
\end{align*}$$
In the constrained optimization problem shown in (19), we suppose that \( X \cap F \neq \emptyset \) holds. Then we look for the best feasible solution \( \hat{x} \in F \) of the constraint satisfaction problem that minimizes the isolation characteristic \( \Gamma_1(\hat{x}, \omega) \) in (9). The isolation characteristic is used both as the objective function \( f(\hat{x}) \) and the constraint \( g_1(\hat{x}, \omega) \leq 0 \) in (19). This is because a high isolation characteristic between Tx and Rx is desired for the SAW duplexer used in the modern radio circuit [5].

B. Algorithm for Constrained Optimization

In the second stage of the proposed design method, we decide on the best structure of the SAW duplexer from the optimum solution of the constrained optimization problem shown in (19). In order to solve the constrained optimization problem, we propose another version of DE named DECO. The proposed DECO is similar to Generalized DE [25] contrived to solve constrained optimization problems. However, as well as DECS, DECO is an extended version of DDE [23]. Therefore, DECO is based on the steady-state model and generates more than one trial vector \( \tilde{u} \) from one target vector \( \tilde{x} \in P \). Furthermore, in the comparison of \( \tilde{u} \) and \( \tilde{x} \in P \), DECO adopts the following selection criteria originally proposed by Deb [26]:

- If both solutions are infeasible, the one with the smaller penalty function value is preferred.
- If one solution is feasible and the other one is infeasible, the feasible solution wins.
- If both solutions are feasible, the one with the smaller objective function value wins.

The control parameters of DECO are the same as those of DECS. The DECO algorithm works as follows:

[DECO Algorithm]

1) Randomly generate initial \( \tilde{x}_i \in P \ (i = 1, \cdots, N_p) \).
2) For all \( \tilde{x}_i \in P \), evaluate \( g(\tilde{x}_i) \) and \( f(\tilde{x}_i) \). Let \( k=N_p \).
3) If \( k>N_k \) then output the best feasible solution \( \tilde{x}_i \in P \cap F \) that has the minimum \( f(\tilde{x}_i) \) value.
4) For each \( \tilde{x}_i \in P \), repeat \( N_f \) times from Steps 5 to 8.
5) Randomly select \( \tilde{x}_{i1} \), \( \tilde{x}_{i2} \), and \( \tilde{x}_{i3} \) from \( P \).
6) Generate a trial vector \( \tilde{u} \) by (15) and (16).
7) Evaluate \( g(\tilde{u}) \) and \( f(\tilde{u}) \). Let \( k=k+1 \).
8) If one of the following conditions holds, replace \( \tilde{x}_i \in P \) by \( \tilde{u} \) such as \( \tilde{x}_i = \tilde{u} \), \( g(\tilde{x}_i) = g(\tilde{u}) \), and \( f(\tilde{x}_i) = f(\tilde{u}) \).
   \[
   \begin{align*}
   &0 < g(\tilde{u}) \leq g(\tilde{x}_i), \\
   &g(\tilde{u}) = 0 \land g(\tilde{x}_i) > 0, \\
   &g(\tilde{u}) = g(\tilde{x}_i) = 0 \land f(\tilde{u}) \leq f(\tilde{x}_i).
   \end{align*}
   \]
9) Return to Step 3.

C. Result of Computational Experiment

Considering the design parameters of the feasible solutions shown in Fig. 4, we reduced the SAW duplexer’s design space given by Table I. First of all, we fixed the values of sensitive design parameters to constant values. Besides, in order to miniaturize the SAW duplexer’s size, we also fixed the length of electrodes to the minimum value in the feasible region. As a result, we could reduce the number of design parameters to be optimized from \( D=18 \) to \( D=11 \). Furthermore, we narrowed the search range \([\tilde{x}_j, \tilde{x}_j] \) of several design parameters given by Table I. Then we defined the reduced design space of the SAW duplexer \( \tilde{X} \subseteq X \) as shown in Table II, in which design parameters fixed to constants are putted in parentheses.

Like the constraint satisfaction problem, the upper and lower bounds of the performance criteria were specified at 301 frequency points \( \omega \in \Omega \) within the range: 700~1000[MHz]. The proposed DECO was also coded by Java and executed on the commodity computer. The control parameters of DECO were the same as those of DECS. Thereby, DECO was applied to the constrained optimization problem shown in (19).

The design parameters’ values \( x_j \ (j = 1, \cdots, D) \) of the optimum solution obtained by DECO are plotted by solid line in Fig. 9, where the respective values are normalized between 0 and 1. As shown in Table II, several design parameters in Fig. 9 have been fixed in advance. The maximum and minimum values of all design parameters \( x_j \ (j = 1, \cdots, D) \) included in the feasible solutions \( \tilde{x} \in A \subseteq F \) are plotted again by broken lines in Fig. 9, where they have been already shown in Fig. 4.

<table>
<thead>
<tr>
<th>( x_j )</th>
<th>( [\tilde{x}_j, \tilde{x}_j] )</th>
<th>( x_j )</th>
<th>( [\tilde{x}_j, \tilde{x}_j] )</th>
</tr>
</thead>
<tbody>
<tr>
<td>( x_1 )</td>
<td>260</td>
<td>( x_{10} )</td>
<td>[45, 65]</td>
</tr>
<tr>
<td>( x_2 )</td>
<td>[3900, 4030]</td>
<td>( x_{11} )</td>
<td>[2.30, 2.40]</td>
</tr>
<tr>
<td>( x_3 )</td>
<td>[18.5, 24.5]</td>
<td>( x_{12} )</td>
<td>24.5</td>
</tr>
<tr>
<td>( x_4 )</td>
<td>[15, 20]</td>
<td>( x_{13} )</td>
<td>18</td>
</tr>
<tr>
<td>( x_5 )</td>
<td>[35, 65]</td>
<td>( x_{14} )</td>
<td>[45, 65]</td>
</tr>
<tr>
<td>( x_6 )</td>
<td>2.42</td>
<td>( x_{15} )</td>
<td>2.26</td>
</tr>
<tr>
<td>( x_7 )</td>
<td>260</td>
<td>( x_{16} )</td>
<td>[55, 95]</td>
</tr>
<tr>
<td>( x_8 )</td>
<td>[3900, 4000]</td>
<td>( x_{17} )</td>
<td>[35, 65]</td>
</tr>
<tr>
<td>( x_9 )</td>
<td>[55, 95]</td>
<td>( x_{18} )</td>
<td>2.07</td>
</tr>
</tbody>
</table>

\( x_1 \): length of electrodes in all filters [um]
\( x_6 \): finger pitch of IDT in Tx filter [um]
\( x_7 \): length of electrodes in all resonators [um]
\( x_{12} \): number of center IDT’s fingers in Rx filter
\( x_{13} \): number of side IDT’s fingers in Rx filter
\( x_{14} \): finger pitch of IDT in Rx filter [um]
\( x_{18} \): finger pitch of IDT in Rx resonators [um]

The frequency response characteristics of the SAW duplexer achieved by the optimum solution are shown in Figs. 10 and 11. Fig. 10 shows the band-pass characteristics of Tx to ANT and ANT to Rx with their upper and lower bounds. Fig. 11 shows the isolation characteristics with its upper bound. From Figs. 10 and 11, we can confirm that the optimum solution is feasible. Furthermore, from the comparison of Fig. 6 and Fig. 11, the isolation characteristic in Fig. 11 is decreased obviously.
VII. CONCLUSION

A computer-aided two-stage design method was proposed and applied to the structural design of a practical SAW duplexer. The SAW duplexer consisted of two DMS filters and four SAW resonators fabricated on a piezoelectric substrate. Due to the complexity of its structure, it was difficult to formulate the structural design of the SAW duplexer as an optimization problem directly. Therefore, we contrived the two-stage design method for the structural design of the SAW duplexer. In the first stage of the proposed design method, the structural design of the SAW duplexer was formulated as a constraint satisfaction problem. Then, in order to obtain feasible solutions as many as possible, a new version of DE called DECS was applied to the constraint satisfaction problem. In the second stage of the proposed design method, considering the information about the SAW duplexer obtained in the first stage and the knowledge of engineers, the structural design of the above SAW duplexer is reformulated again as a constrained optimization problem. In order to an optimum solution of the constrained optimization problem effectively, namely an optimal structure of the SAW duplexer, another version of DE called DECO was applied to the constrained optimization problem. Finally, through the structural design of the practical SAW duplexer, the usefulness of the proposed two-stage design method was demonstrated.

The proposed two-stage design method can be applied to other SAW devices. Furthermore, the concept two-stage design method is applicable to various real-world applications.

Although the two-stage design method showed promising results, there are several issues which need to further research. If we can’t find any feasible solution in the first stage, we have to loosen several constraints of the constraint satisfaction problem. Considering the result of the first stage, we may fix several design parameters to constants in the second stage. However, it doesn’t make any guarantee that the constrained optimization problem has feasible solutions. If we can’t find any feasible solution for the problem, we need to reconsider the design parameters to be fixed. In any case, the results of the proposed design method rely on the searching abilities of the optimization algorithms used in the first and second stages. Therefore, in our future works, we would like to improve DECS and DECO further by using the latest optimization techniques.

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