A Testing Theory for Real-Time Systems

Stefan D. Bruda and Chun Dai

Abstract—We develop a testing theory for real-time systems. We keep the usual notion of success or failure (based on finite runs) but we also provide a mechanism of determining the success or failure of infinite runs, using a formalism similar to the acceptance in Büchi automata. We present two refinement timed preorders similar to De Nicola and Hennessy’s may and must testing. We then provide alternative, behavioural and language-based characterizations for these relations to show that the new preorders are extensions of the traditional preorders. Finally we focus on test generation, showing how tests can be automatically generated out of a timed variant of linear-time logic formulae (namely, TPTL), so that a process must pass the generated test if and only if the process satisfies the given temporal logic formula. Beside the obvious use of such an algorithm (to generate tests), our result also establishes a correspondence between timed must testing and timed temporal logic.

Index Terms—Formal methods, Real-time systems, Model-based testing, May testing, Must testing, Testing preorders, Test generation, Timed temporal logic, TPTL

I. INTRODUCTION

How to guarantee validity and reliability of software and hardware is one of the most pressing problems. Conformance testing [1] is known in this area for its succinctness and high automatization. Its aim is to check whether an implementation conforms to a given specification.

In the context of conformance testing system specifications can be mainly classified into two kinds: algebraic and logic. The first favours refinement, where a single algebraic formalism is equipped with a refinement relation to represent and relate both specifications and implementations [2]. An implementation is validated if it refines its specification. Process algebrae, labelled transition systems, and finite automata are commonly used in this classification, with traditional refinement relations being either behavioural equivalences or preorders [3], [4]. A typical example is model-based testing [3]. The second approach prefers assertive constructs. Different formalisms describe the properties of specifications and implementations; specifications are defined logically while implementations are given in an operational notation. The semantics of assertions is to determine whether an implementation satisfies its specification. A typical example is model checking [5].

The domain of conformance testing consists in reactive systems, which interact with their environment. Often such systems are required to be real-time, meaning that in addition to the correct order of events, they must satisfy constraints on delays separating certain events. Real-time specifications are then used as the basis of conformance testing for such systems.

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The aim of this paper is to develop a semantic theory for real-time system specification based on timed transition systems modeling the behaviour of real-time processes. We first develop a semantic theory for real-time system specification. Using a theory of timed ω-final states (inspired from the theory of timed automata [6]) as well as a timed testing framework based on De Nicola and Hennessy’s testing [4], we develop timed may and must preorders that relate timed processes on the basis of their responses to timed tests. Our framework is as close to the original framework of (untimed) testing as possible, and is also as general as possible.

While studies of real-time testing abound, they mostly restrict the real-time domain to make it tractable. We believe that starting from a general theory is more productive than starting directly from some practically feasible (and thus restricted) subset of the issue, so our theory is general. Still, our general approach is more practical than one might expect. Indeed, the characterization of the timed preorders uses a surprisingly concise set of timed tests.

To further address the practicality issue we then tackle now automatic test generation. We show how to algorithmically build tests starting from timed propositional temporal logic (or TPTL) [7] formulae. This algorithm is also a first yet significant step toward an integration of operational and assertive specification styles in the area of real-time systems, to obtain heterogeneous (algebraic and logic) specifications and tools.

The remainder of this paper is structured as follows. Preliminaries such as preorders, timed automata, and timed transition systems are presented in the next section. We formalize the notion of timed processes and timed tests in Section III, where we also introduce our timed preorders. Section IV characterizes the timed preorders and Section V presents our conversion of TPTL formulae into equivalent timed tests. We conclude in Section VI.

II. PRELIMINARIES AND NOTATIONS

Preorders are reflexive and transitive relations. They are widely used as implementation relations comparing specifications and implementations. Preorders are easier to construct and analyze compared to equivalence relations, and once a preorder is established, an associated equivalence relation is immediate. The cardinality of \( \mathbb{N} \) is denoted by \( \omega \).

Our constructions are based on some alphabet \( A \) representing a set of actions excluding the internal action \( \tau \) and on a time alphabet \( L \) which contains some kind of positive numbers (such as \( \mathbb{N} \) or \( \mathbb{R}^+ \)). A set of clocks \( C \) is a set of variables over \( L \). We use \( A, L, \) and \( C \) in sans-serif face exclusively for this purpose, so that their purpose is often consider understood throughout the paper.

A clock interpretation for a set \( C \) of clocks is a mapping \( C \rightarrow L \). If \( t > 0 \) and \( c \) is a clock interpretation over \( C \), in the
clock interpretation \( c' = c + t \) we have \( c'(x) = c(x) + t \) for all clocks \( x \in C \). Clocks can also be reset to zero.

If \( x \) is a clock and \( r \) is a real then \( x \sim r \) is a time constraint, \( \sim \in \{ =, <, =, \neq, >, \geq \} \). Constraints can be joined in conjunctions or disjunctions, together with the special constraint \( \top \) (which is true in any interpretation). \( \Pi(C) \) denotes the set of all time constraints over a set \( C \) of clocks.

A. Timed Transition Systems

Labelled transition systems [3] are used to model the behaviour of various processes; they serve as a semantic model for formal specification languages. A timed transition system is essentially a labelled transition system extended with time values associated to actions. Timed automata [6], [8] are based on the automata theory and introduce the notion of time constraints over their transitions. In general labelled transition systems model the execution of a process, while timed automata are suitable for specifying processes or defining tests upon processes. We find convenient to combine the two concepts to obtain a unified model for real time, which we call by abuse of terminology (and for lack of a better term) timed transition system. We do not introduce any new concept in this section; instead we unify existing constructions into a convenient single construction. A timed transition system is essentially a timed automaton (or more precisely a timed transition table, since final states will be introduced later) without the restriction of the number of states being finite.

For a set \( A \) of observable actions (\( \tau \notin A \)), a set \( L \) of times values, and a set \( C \) of clocks, a timed transition system is a tuple \( (A \times L) \cup \{ \tau \}, C, S, \rightarrow, p_0 \) where: \( S \) is a countable set of states; every state \( p \in S \) has an associated clock interpretation \( c_P : C \rightarrow L \); \( \rightarrow \subseteq (S \times (A \times L)) \times S \times \Pi(C) \times 2^C \times (S \times \{ \tau \} \times S) \) is the transition relation (we use \( p_x^{(a, \delta)} \) instead of \( (p, (a, \delta), p', t, C) \in \rightarrow \), omitting \( C \) whenever \( C = \emptyset \) and also \( t \) whenever \( t = \top \)); \( p_0 \) is the initial state.

Whenever \( p_x^{(a, \delta)} \), the transition system performs \( a \) with delay \( \delta \); the delay causes the clocks to progress so that \( c_P(x) = c_P(x) + \delta \) whenever \( x \notin C \) and \( c_P(x) = 0 \) otherwise; the transition is enabled only if \( t \) holds under the interpretation \( c_P \); \( \tau \) transitions do not affect clock interpretations and cannot be time constrained. Normally a trace is described as a sequence of the events or states (but not the delays between them). To add time to a trace, we add time information to the usual notion of trace (that contains actions only). A timed trace over \( A, L, \) and \( C \) is a member of \((A \times L \times \Pi(C) \times 2^C)^* \cup (A \times L \times \Pi(C) \times 2^C)^{\omega} \).

If both \( L \) and \( \Pi(C) \) (or equivalently \( C \)) are empty the timed transition system becomes an LTS, and its timed traces are normal traces. One of \( L \) or \( \Pi(C) \) could be empty and we still obtain a timed trace; we will use this to differentiate between processes and specifications.

As usual we write \( p_x^{(a, \delta)} p' \) if and only if \( p \xrightarrow{a, \delta} \cdots \xrightarrow{a, \delta} p^{(a, \delta)}_x p' \) and \( p \xrightarrow{a, \delta} p' \) if and only if \( p = p_0 \xrightarrow{a, \delta} \cdots \xrightarrow{a, \delta} p_n = p' \), for some \( n \geq 0 \). Furthermore \( p \xrightarrow{w} q \) whenever \( w = (a_1, \delta_1, t_1, C_1)_{0 \leq i < k} \) and \( p = p_{(a_1, \delta_1)}^{(a_2, \delta_2)}_{(a_3, \delta_3)} \cdots p_k = p' \), for some \( k \geq 0 \). A timed path \( \Pi(p') \) is init\( p' \) if and only if \( \exists \Pi(p) \neq \Pi(p') \)

That a process \( p \) cannot evolve any further (via either internal or external actions) is denoted by \( p \not\rightarrow \).

A timed path \( \pi \) of a timed transition system \( M = ((A \times L) \cup \{ \tau \}, C, S, \rightarrow, p_0) \) is a potentially infinite sequence \((p_{i-1}, (a_i, \delta_i, t_i, C_i), p_i)_{i \leq k} \), where \( p_0 = \Pi(p) \) for all \( 0 < i \leq k \); the length of \( \pi \) is \( k \) and is denoted by \( \| \pi \| \). If \( |\pi| = \omega \), \( \pi \) is infinite; otherwise (that is, if \( |\pi| \leq n \) \( n \) is finite. If \( \| \pi \| \in \mathbb{N} \) and \( p_\pi \not\rightarrow \) (that is, \( p_\pi \) is a deadlock state), then the timed path \( \pi \) is called maximal trace(\( \pi \)), the (timed) trace of \( \pi \) is defined as the sequence \((a_i, \delta_i, t_i, C_i)_{0 \leq i \leq k} \in (A \times L \times \Pi(C) \times 2^C)^* \cup (A \times L \times \Pi(C) \times 2^C)^\omega \). \( \Pi(p'), \Pi_m(p'), \Pi_f(p') \) denote the sets of all finite timed paths, all maximal timed paths, and all infinite timed paths starting from state \( p' \in S \), respectively. We also put \( \Pi(p') = \Pi(p') \cup \Pi_m(p') \cup \Pi_f(p') \). The empty timed path \( \pi \) with \( |\pi| = 0 \) is symbolized by (\( \pi \)) and its (always empty) trace by \( \epsilon \).

State \( p' \) of transition system \( p \) is timed divergent, denoted by \( p' \not\rightarrow_P \) (or just \( p' \uparrow \) when there is no ambiguity), if \( \exists \Pi(p') : \text{trace}(\pi) = \epsilon \). State \( p' \) is called timed \( \omega \)-divergent (denoted by \( p' \not\rightarrow \omega \) \( \uparrow \)) for some \( \omega = (a_0, \delta_0, t_0, C_0)_{0 \leq k} \in (A \times L \times \Pi(C) \times 2^C)^* \cup (A \times L \times \Pi(C) \times 2^C)^\omega \). \( \Pi(p') \) is a timed convergent or timed \( \omega \)-convergent (\( p' \not\rightarrow_P \omega \uparrow \)), respectively, omitting the subscript \( p \) when there is no ambiguity) if it is not the case that \( p' \not\rightarrow_P \) \( \uparrow \) \( \omega \). The set of initial actions of \( p' \) is init\( p' = \{ (a, \delta, t, C) \in A \times L \times \Pi(C) \times 2^C : \exists \Pi(p') : p'(a, \delta, t, C) \} \).

Definition 1: TIMED TRACE LANGUAGES. For a timed transition system (state) \( p \) the timed finite-trace language \( L_f(p) \), maximal-trace (complete-trace) language \( L_m(p) \), infinite-trace language \( L_\infty(p) \), and divergence language \( L_D(p) \) of \( p \) are

\[
L_f(p) = \{ \text{trace}(\pi) \in \Pi(p) \}
\]

\[
L_m(p) = \{ \text{trace}(\pi) \in \Pi_m(p) \}
\]

\[
L_\infty(p) = \{ \text{trace}(\pi) \in \Pi_f(p) \}
\]

\[
L_D(p) = \{ w \in (A \times L \times \Pi(C) \times 2^C)^* \cup (A \times L \times \Pi(C) \times 2^C)^\omega : p' \not\rightarrow_P \omega \downarrow_w \}
\]

We defined timed languages slightly differently from the original [6] to reflect their use for system specification and also to simplify the presentation. However if we omit the clocks and their constraints (which we will do for processes) there is a natural bijection between our definition and the original.

Once more similar to the theory of timed automata [6] we introduce a set of timed \( \omega \)-final states. Then:

Definition 2: TIMED \( \omega \)-REGULAR TRACE LANGUAGE. The timed \( \omega \)-regular trace language of some timed transition system \( p \) is \( L_\omega(p) = \{ \text{trace}(\pi) : \pi \in \Pi_\omega(p) \} \subseteq (A \times L \times \Pi(C) \times 2^C)^\omega \), where \( \Pi_\omega(p) \) contains exactly all the \( \omega \)-regular timed paths; that is, \( \omega \)-final states must occur infinitely often in any \( \pi \in \Pi_\omega(p) \).

We exclude henceforth Zeno behaviours from all the languages that we consider; that is, no trace is allowed to show Zeno behaviour. In other words, time progresses and must eventually grow past any constant value (this property is also called progress [6], [9]).
B. Timed Propositional Temporal Logic

Timed Propositional Temporal Logic (TPTL) [7] is one of the most general temporal logics with time constraints [10]. TPTL extends linear-time temporal logic (LTL) [5], [10] by adding time constraints, so that its semantics is given with respect to timed traces\(^1\), that is, timed words in \((A \times L)^* \cup (A \times L)^\omega\). We use TPTL without congruence, but we just call it TPTL for short.

For presentation convenience we use a slightly modified form of TPTL without congruence. However, it is immediate that our form is equivalent to the original so we continue to use this form of TPTL without congruence. However, it is immediate that our form is equivalent to the original so we continue to use this form of TPTL without congruence. Henceforth implied.

fact shorten this to just TPTL, the lack of congruence being henceforth implied.

With \(\phi, \phi_1, \phi_2\) ranging over TPTL formulas, \(a\) ranging over \(A\), \(x\) ranging over a set of clocks \(C\), and \(c\) ranging over positive constants, the syntax of the term \(\theta\) and the TPTL formula \(\phi\) is the following:

\[
\theta = x + c \mid c
\]

\[
\phi = \theta_1 \leq \theta_2 \mid \top \mid a \mid \neg \phi \mid \phi_1 \land \phi_2 \mid X \phi \mid \phi_1 U \phi_2 \mid x.\phi
\]

Let \(\mathcal{F}\) be the set of all TPTL formulas. A timed trace \(w = (a_1, \delta_1)_{0 \leq i \leq k} \in (A \times L)^* \cup (A \times L)^\omega\) satisfies \(\phi\) if and only if \(w \models \phi\). The relation \(\models_{\mathcal{F}} \subseteq ((A \times L)^* \cup (A \times L)^\omega) \times \mathcal{F}\) is the least relation satisfying the conditions in the semantics of TPTL formulae shown below, with \(w_i\) standing for \((a_i, \delta_i)_{0 \leq i \leq k}\) for any \(1 \leq i \leq k\), and \(\gamma : C \rightarrow L\) being some clock interpretation.

- \(\theta_1 \leq \theta_2\) if and only if \(\gamma(\theta_1) \leq \gamma(\theta_2)\),
- \(w \models_{\mathcal{F}} \top\) and \(w \models_{\mathcal{F}} \bot\) for any \(w\),
- \(w \models_{\mathcal{F}} a\) if and only if \(w \neq \varepsilon\) and \(a_1 = a\),
- \(w \models_{\mathcal{F}} \neg \phi\) if and only if \(\neg(w \models_{\mathcal{F}} \phi)\),
- \(w \models_{\mathcal{F}} \phi_1 \land \phi_2\) if and only if \(w \models_{\mathcal{F}} \phi_1\) and \(w \models_{\mathcal{F}} \phi_2\),
- \(w \models_{\mathcal{F}} X \phi\) if and only if \(w_{i+1} \models_{\mathcal{F}} \phi\) for all \(i \in C \) for \(\exists \leq i \leq k\): \(\forall i \leq r \leq k: \),
- \(w \models_{\mathcal{F}} \phi_i U \phi_2\) if and only if \(w_{i+1} \models_{\mathcal{F}} \phi_2\) for all \(i \in C \) for \(\exists \leq i \leq k\): \(\forall i \leq r \leq k: \),
- \(w \models_{\mathcal{F}} \gamma \leq \gamma(1)\), and \(w \models_{\mathcal{F}} \phi\) if and only if \(w \models_{\mathcal{F}} (\gamma, \phi)\).

We denote by \(\gamma + c\) a clock interpretation in which \((\gamma + c)(x) = \gamma(x) + c\) for all clocks \(x\). We require that \(\gamma(x + c) = \gamma(x) + c\) and \(\gamma(c) = c\); \(\gamma(t/x)\) is the clock interpretation that agrees with \(\gamma\) on all clocks except \(x\), which is mapped to \(t \in L\). The occurrence of a free time variable \(x\) in a formula “freezes” the moment in time, which can be checked later by using \(x\) in various expressions. These restrictions are sufficient to model most phenomena from other timed temporal logics [7].

As usual one can also introduce the derived operators \(G\) (“globally”) and \(F\) (“eventually”) as \(G \phi = \top \Rightarrow R \phi\) and \(F \phi = \top \Rightarrow U \phi\), respectively. The operator \(R\) (“releases”) is the dual of the operator \(U\). A timed process\(^2\) \(p\) satisfies the TPTL formula \(\phi\), written \(p \models \phi\), if and only if \(\forall w \in L_f(p) \cup L_m(p) \cup L^\omega(p) \cup L_D(p): w \models \phi\).

III. A Testing Theory for Real Time

We are now ready to extend the testing theory of De Nicola and Hennessy [4] in two ways. For one thing, we adapt this testing theory to timed testing. In addition, we are also introducing the concept of Büchi acceptance to tests (or Büchi success), so that the properties of infinite runs of a process can be readily identified by tests. Timed testing has been studied before in many contexts [8], [11], [12] but to our knowledge never in such a general setting and never including Büchi success. In addition, timed testing has never been considered in conjunction with test generation from temporal logic formulae. We note however that a somehow incipient consideration of Büchi success for tests and also of temporal logic formulae as test generators for untimed tests exists [13], though this theory is not real time and to our knowledge has not been pursued any further.

The traditional testing framework defines behavioural preorders that relate labelled transition systems according to their responses to tests [4], [14]. The tests are thus used to verify the external interactions between a system and its environment. We use timed transition systems as the formalism for both processes and tests.

In our framework a test is a timed transition system where certain states are considered to be success states. In order to determine whether a system passes a test, we run the test in parallel with the system under test and examine the resulting finite or infinite computations until the test runs into a success state\(^3\) (pass) or a deadlock state (fail). In addition, a set of \(\omega\)-final states is used to compartmentalize infinite runs into successful and unsuccessful.

Definition 3: Timed Processes and Tests. A timed process \((A \times L) \cup \{\tau\}, S, \rightarrow, p_0\) is a timed transition system \((A \times L) \cup \{\tau\}, S, \rightarrow, p_0\) with an empty set of clocks (and thus with no time constraints). It follows that all the traces of any timed process are in the set \((A \times L)^* \cup (A \times L)^\omega\).

A timed test \((A \cup \{\tau\}, C, T, \rightarrow_t, \Sigma, \Omega, t_0)\) is a timed transition system \((A \cup \{\tau\}, C, T, \rightarrow_t, \Sigma, \Omega, t_0)\) with the addition of \(\Sigma \subseteq T\) of success states and \(\Omega \subseteq T\) of \(\omega\)-final states. Note that \(L = \emptyset\) for tests and therefore \(\rightarrow_t \subseteq (T \times A \times \Sigma) \times 2^C \times T\) \(\cup (T \times \{\tau\} \times T\).

The transition relation of a process and a test are restricted in (different manners) because the test runs in parallel with the process under the test\(^4\). This latter process (called the implementation) features time sequences but no time constraints, while the test features only time constraints. It is meaningless to run the test by itself. If \(\Sigma(C) = \emptyset\) which means there is no time constraint in the test, we call the test classical. The set of all timed tests is denoted by \(T\).

Definition 4: Partial Computation. A partial computation \(c\) of a timed process \(p\) and a timed test \(t\) is a potentially

\(^1\)Time traces as presented in the previous section also contain time constraints; however, as we will see in Section III, the time constraints appear only in tests and so the trace of processes are over \(A \times L\) only.

\(^2\)A timed process is a timed transition system without time constraints, as detailed in Section III

\(^3\)Success states are deadlock states too, but we distinguish then as special deadlock states.

\(^4\)Note however that the difference is syntactical only, for indeed the transition relation for a timed process allows for an empty set \(L\).
infinite sequence \( (p_{i-1}, t_{i-1})_{i < k} \), where \( k \in \mathbb{N} \cup \{0\} \), such that \( p_i \in P \) and \( t_i \in T \) for all \( 0 < i < k \); and \( \delta_i \in L \) is taken from \( p_i \), \( t_i \) and \( C_i \) are taken from \( t_i \) and \( R \in \{1, 2, 3\} \) for all \( 0 < i \leq k \). The relation \( \rightarrow \) is defined by the following rules:

1. \( (p_{i-1}, t_{i-1}) \rightarrow (p_i, t_i) \) if \( a_i = \tau \) and \( p_{i-1} \rightarrow \tau_p p_i \) or \( t_{i-1} \rightarrow \tau_t t_i \) and \( t_i \notin \Sigma \).
2. \( (p_{i-1}, t_{i-1}) \rightarrow \sigma (p_i, t_i) \) if \( a_i = \sigma \) and \( p_{i-1} \rightarrow \tau_p p_i \) or \( t_{i-1} \rightarrow \tau_t t_i \) and \( t_i \notin \Sigma \).
3. \( (p_{i-1}, t_{i-1}) \rightarrow \omega (p_i, t_i) \) if \( a_i, \delta_i \in A \times L \), \( p_{i-1} \rightarrow (a_i, \delta_i) p_i \) and \( t_{i-1} \rightarrow (\tau_t, \delta_t) t_i \) and \( t_i \notin \Sigma \).

The first two expressions in the definition of \( \rightarrow \) indicate that when the test or the process under test is executing an internal action, the other process keeps its state. The third expression indicates that when the action is not internal, the test and the process under test execute their respective action in parallel, and spend the same time while doing so. The test also needs to check its time constraint.

If \( k \in \mathbb{N} \) then \( c \) is finite, denoted by \( |c| < \omega \); otherwise, it is infinite, that is, \( |c| = \omega \).

The projection \( \text{proj}_p(c) \) of \( c \) on \( p \) is defined as \( (p_{i-1}, (a_i, \delta_i), p_i) \in \prod(p) \), where \( \prod(p) = \{0 < i \leq k \mid R_i \in \{1, 3\}\} \). Similarly, the projection \( \text{proj}_t(c) \) of \( c \) on \( t \) as \( t_{i-1} = (a_i, \delta_i, t_i, C_i), t_i \in \prod(t) \), where \( \prod(t) = \{0 < i \leq k \mid R_i \in \{2, 3\}\} \).

**Definition 5:** Computation. A partial computation \( c \) is a computation whenever: If \( k \in \mathbb{N} \) then \( c \) is maximal, that is, \( p_k \rightarrow \tau_p p_{k+1} \) and \( t_k \rightarrow \tau_t t_{k+1} \) and \( \text{init}(p_k) \cap \text{init}(t_k) = \emptyset \) or the time delay of \( \tau \) does not satisfy the time constraint of \( t_k \); If \( k = \omega \) then \( \text{proj}_p(c) \in \Pi_1(p) \). \( C(p, t) \) is the set of all computations of \( p \) and \( t \).

Computation \( c \) is successful if \( t_{|c|} \in \Sigma \) whenever \( |c| \in \mathbb{N} \) and \( \text{proj}_p(c) \in \Pi_0(t) \) whenever \( |c| = \omega \).

**Definition 6:** Timed May and Must Preorders. \( p \) may pass \( t \) (written \( p \Rightarrow_T t \)), if and only if there exists at least one successful computation \( c \in C(p, t) \); \( p \) must pass \( t \) (written \( p \Rightarrow_T t \)) if and only if every successful computation \( c \in C(p, t) \) is successful.

\[ p \Rightarrow_T \omega \] if and only if \( \forall t \in T : p \Rightarrow_T t \implies q \Rightarrow_T t \); and \( p \Rightarrow_T \omega \) if and only if \( \forall t \in T : p \Rightarrow_T t \implies q \Rightarrow_T t \).

Intuitively, an infinite computation of process \( p \) and test \( t \) is successful if the test passes through a set of \( \omega \)-final states infinitely often. Hence some infinite computations can be successful in our setting. Since timed processes and timed tests potentially exhibit nondeterministic behaviour, one distinguishes between the possibility and inevitability of success. It is immediate that the relations \( \Rightarrow_T \omega \) and \( \Rightarrow_T \omega \) are preorders. They are defined analogously to the classical may and must preorders (which are based on labelled transition systems and restrict \( T \) to classical tests).

**IV. Alternative Characterizations of Timed Preorders**

We now present alternative characterizations for the timed may and must preorders. The characterizations are similar in style to other characterizations and provide the basis for comparing the existing testing theory to our timed testing. The first characterization is similar to the characterization of other preorders [14], [15] and relates timed testing directly with the behaviour of processes.

**Theorem 1:**

1. \( p \Rightarrow_T \omega \) if and only if \( L_f(p) \subseteq L_f(q) \) and \( L_m(p) \subseteq L_m(q) \).
2. \( p \Rightarrow_T \omega \) if and only if for all \( w \in (A \times L)^+ \cup (A \times L)^- \) such that \( p \Rightarrow_T w \) it holds that:
   a) \( q \mathrel{\Rightarrow_T} w \)
   b) \( |w| < \omega \) then \( \forall q' : q \Rightarrow_T w \) implies \( \exists q' : p \Rightarrow_T w \) and \( \text{init}(p) \subseteq \text{init}(q) \), and
   c) if \( |w| = \omega \) then \( w \in L_m(p) \) implies \( w \in L_m(q) \).

The second characterization is given in terms of timed trace inclusions, once more similarly to the characterization of other preorders [2], [15]. Note that we are now concerned with \( \Rightarrow_T \omega \) only, as the simplest \( \Rightarrow_T \omega \) is already characterized in terms of timed traces in Theorem 1.

To state this result we need to introduce the notion of pure nondeterminism. We call a timed process \( p \) purely nondeterministic if for all states \( p' \), \( p'' \) \( p \Rightarrow_T \omega \) implies \( p' \Rightarrow_T \omega \), and \( \{(a, \delta, p') : p \Rightarrow_T \omega \} = \{1\} \). Note that every timed process \( p \) can be transformed to a purely nondeterministic timed process \( p' \), such that \( L_f(p) = L_f(p') \), \( L_m(p) = L_m(p') \), and \( L_m(p) = L_m(p') \) by splitting every transition \( \tau_p \) into two transitions \( \tau_p \Rightarrow_T p \) and \( \tau_p \Rightarrow_T p' \), where \( p \Rightarrow_T \omega \) is a new, distinct state.

**Theorem 2:** Let \( p \) and \( q \) be timed processes such that \( p \) is purely nondeterministic. Then \( p \Rightarrow_T \omega \) if and only if all of the following hold:

\[ L_D(p) \subseteq L_D(q) \]
\[ L_f(q) \subseteq L_f(p) \]
\[ L_m(q) \subseteq L_m(p) \]
\[ L_m(q) \subseteq L_m(p) \]
\[ L_m(q) \subseteq L_m(p) \]

With respect to finite traces, the characterizations of timed tests differ from the ones of classical preorders by the addition of time variables. We also need to refine the classical characterizations so as to capture the behaviour of timed may- and must-testing with respect to infinite traces. The proofs of the characterization theorems 1 and 2 rely on the properties of the following specific timed tests.

- For \( w = (a_i, \delta_i)_{0 < i < k} \in (A \times L)^k \), let \( \mathcal{L}^{w, \omega} = (A \cup \{\tau\}, C, T, \rightarrow, \emptyset, k, 0, 0, \delta) \), where \( T = \{0, 1, \ldots, k\} \), \( \rightarrow = \{(i - 1, a_i, \delta_i) : 0 < i \leq k \} \), \( \delta = \{(k, k, k, k, k)\} \).
- For \( w = (a_i, \delta_i)_{0 < i < k} \in (A \times L)^k \), let \( \mathcal{L}^{w, \omega} = (A \cup \{\tau\}, C, T, \rightarrow, \{k\}, 0, 0, \delta) \), where \( T = \{0, 1, \ldots, k\} \), \( \rightarrow = \{(i - 1, a_i, \delta_i) : 0 < i \leq k \} \), \( \delta = \{(k, k, k, k)\} \).
- For \( w = (a_i, \delta_i)_{0 < i < k} \in (A \times L)^k \), let \( \mathcal{T}^{w, \omega} = (A \cup \{\tau\}, C, T, \rightarrow, \emptyset, s, s) \), where \( T = \{0, 1, \ldots, k\} \cup \{s\} \),
Fig. 1. Timed tests used for the characterization of timed may and must preorders.

- For \( w = (a_i, \delta_i)_{0 < i \leq k} \in (A \times L)^* \), let \( t_w^\omega = (A \cup \{\tau\}, C, T, \rightarrow, \{s\}, 0, \{s\}) \), where \( T = N \cup \{s\} \), \( \rightarrow = \{(i-1, a_i, i, c_i = \sum_{j=0}^i \delta_i) : 0 < i \leq k \} \) \cup \{(i, \tau, s, T) : i > 0 \} \).
- For \( w = (a_i, \delta_i)_{0 < i \leq k} \in (A \times L)^* \), let \( t_{w,M}^\omega = (A \cup \{\tau\}, C, T, \rightarrow, \emptyset, 0, \{s\}) \), where \( T = \{0, 1, \ldots, k\} \cup \{s\} \), \( \rightarrow = \{(i-1, a_i, i, c_i = \sum_{j=0}^i \delta_i) : 0 < i \leq k \} \) \cup \{(i, \tau, s, T) : 0 \leq i < k \} \)
- For \( w = (a_i, \delta_i)_{0 < i \leq k} \in \text{A} \subseteq (A \times L)^* \), let \( t_{w,A}^\omega = (A \cup \{\tau\}, C, T, \rightarrow, \emptyset, 0, \{s\}) \), where \( T = \{0, 1, \ldots, k\} \cup \{s\} \), \( \rightarrow = \{(i-1, a_i, i, c_i = \sum_{j=0}^i \delta_i) : 0 < i \leq k \} \) \cup \{(i, \tau, s, T) : 0 \leq i < k \} \)
of a potential computation $c$ for both $p$ and $t_{May^*, w}$. In fact $w$ is even the trace of a computation of $p$ and $t_{May^*, w}$ (indeed, $t_{May^*, w} \not\models \tau$ and $I_{p_k}(p_k) \cap I_{t_{May^*, k}}(k) = \emptyset$, and it is further the trace of a successful computation (since $k \in Suc$). It then follows that $p \models t_{May^*, w}$.

$\leftarrow$: Given that $p \models t_{May^*, w}$, we have a successful computation $c$ for $p$ and $t_{May^*, w}$. That is, $(p_{i-1}, t_{i-1}, \cdots, p_t, t_k, p_k, t_{May^*, w}) \models \tau$, $I_{p_k}(p_k) \cap I_{t_{May^*, k}}(k) = \emptyset$, and $t_{May^*, w} = t_{May^*, w}^*$. By a reverse argument we conclude then that $w \in L_f(p) \cap t_{May^*, w}^*$, which means that $p \not\models t_{May^*, w}^*$ and so it must be that $w \not\in L_f(p)$.

$\Rightarrow$ Assume that $p \not\models t_{May^*, w}^*$. Then, for some $0 < l < k$ there exists one trace $p_0 (a_1) p_1 (a_2) \cdots (a_l) p_l \cdots$ which fails the test $t_{May^*, w}^*$. This contradicts the condition $p \models t_{May^*, w}^*$ and so it must be that $p \models t_{May^*, w}^*$.

$\Leftarrow$: Assume that $w \in L_f(p)$. By the definition of $t_{May^*, w}^*$, $w$ fails to pass this test. This contradicts the condition that $p \models t_{May^*, w}^*$.

Item 5 is proven similarly.

Item 6, $\Rightarrow$: Assume that $p \not\models t_{May^*, w}^*$ does not hold. According to the definition of $t_{May^*, w}^*$, there are two ways for $p$ to fail the test: Either $p_0 (a_1) p_1 (a_2) \cdots (a_l) p_l \cdots$ or $p_0 (a_1) p_1 (a_2) \cdots (a_l) p_l \cdots$ which fail the test $t_{May^*, w}^*$. These contradict the conditions $p \not\models t_{May^*, w}^*$ and so it must be that $p \models t_{May^*, w}^*$.

Items 7 and 8 are proven similarly.

The proof of Theorem 1 relies extensively on these intuitive properties of timed tests. Notice that the usage of $\omega$-state tests (that is, tests that accept based on an acceptance family, not only on $Suc$)—even when discussing finite-state timed processes—is justified by our view that timed tests represent the arbitrary, potentially irregular behaviour of the unknown real-time environment.

**Proof Theorem 1**: Item 1 of the theorem is fairly immediate. For the $\Rightarrow$ direction we distinguish the following cases: $w \in L_f(p)$ implies that $p \models t_{May^*, w}$, and $w \in L_f(p)$ has two sub-cases:

(a) If $|w| = \omega$, then $p \models t_{May^*, w}$.

(b) If $|w| < \omega$, then $p \models t_{May^*, w}$. Since $p \not\models q$ it follows that $q \not\models t_{May^*, w}$ and thus $w \not\in L_f(q)$.

We go now to the $\Leftarrow$ direction for Item 1. Let $t$ be any timed process such that $p \models t_{May^*, t}$, that is, there exists a successful computation $c \in C(p, t)$ with $w = trace(proj_p(c)) = \sum_{j=0}^i \delta_i : 0 < i \leq k) \cup \{(i, \tau, s, T) : 0 \leq i < k\} \cup \{(k, a, s_2, T) : a \in A\}$.

These tests are depicted graphically in Figure 1. In the figure $\omega$-final states are marked by the symbol $\omega$ and success states are distinguished from regular states by thick borders.

Intuitively, while timed tests $t_{May^*, w}$ and $t_{May^*, w}^*$ test for the presence of a finite and infinite trace $w$, respectively, timed tests $t_{May^*, div}, t_{May^*, w}^*$, and $t_{May^*, w}^*$ are capable of detecting divergent behaviour when executing trace $w$. These are “presence” tests, that check whether a trace $w$ (finite or infinite) exists in the implementation. Timed tests $t_{Must^*, w}, t_{Must^*, w}^*$, and $t_{Must^*, w}^*$ test for the absence of the finite trace, maximal trace, and $\omega$-state trace (that is, trace that goes through infinite occurrences of $\omega$-final states) $w$, respectively.

Timed must-testing is a bit trickier, since we cannot feasibly check all the possible traces or computations exhaustively (as we need to do according to the definition of must testing). So we think the other way around: We assume one “failure trace,” which does not satisfy the test and leads to failure. If there exists at least one such failure trace, then the test fails. On the other hand, if we cannot find the failure trace in the implementation, the test succeeds. We then test the absence of this trace in must-testing.

Finally, timed test $t_{May^*, w}^*$ is capable of comparing the initial action sets of states reached when executing trace $w$ with respect to a subset $A \subseteq A$.

Note that we use the tightest time constraint possible in our test. We denote $c_i = \sum_{j=0}^i \delta_i$ by $t_i$ in what follows.

Our specific timed tests satisfy the following desired properties:

**Lemma 3:**

1) Let $w \in (A \times L)^*$. Then, $w \in L_f(p)$ if and only if $p \models t_{May^*, w}$.

2) Let $w \in (A \times L)^\omega$. Then, $w \in L_w(p)$ if and only if $p \models t_{May^*, w}$.

3) Let $w \in (A \times L)^*$. Then, $w \in L_w(p)$ if and only if $p \models t_{May^*, div}$.

4) Let $w \in (A \times L)^*$. Then, $p \not\models w$ if and only if $p \models t_{May^*, w}^*$.

5) Let $w \in (A \times L)^* \cup (A \times L)^\omega$. Then, $p \not\models w$ if and only if $p \models t_{May^*, w}^*$.

6) Let $w \in (A \times L)^*$ such that $p \not\models w$. Then, $w \not\in L_f(p)$ if and only if $p \models t_{Must^*, w}$.

7) Let $w \in (A \times L)^*$ such that $p \not\models w$. Then, $w \not\in L_m(p)$ if and only if $p \models t_{Must^*, w}$.

8) Let $w \in (A \times L)^*$ such that $p \not\models w$. Then, $w \not\in L_w(p)$ if and only if $p \models t_{Must^*, w}$.

**Proof:** The proofs are simple analyses of the potential computations arising when running the timed tests in lock-step (to a deadlock or successful state) with arbitrary timed processes. Let $w = (a_1, \delta_t)_{0 \leq t \leq k}$ for some $k \in \mathbb{N} \cup \{\omega\}$.

- **Item 1,** $\Rightarrow$: $w \in L_f(p)$, and thus $p_0 (a_1) p_1 (a_2) \cdots (a_l) p_t (\cdots) \models \tau$ (Definition 1). On the other hand, $t_{May^*, w}^* (a_1) t_{May^*, w}^* (a_2) \cdots (a_l) t_{May^*, w}^* k$ (definition of $t_{May^*, w}^*$ including the form of $t_i$). Therefore, $(p_{i-1}, t_{i-1}, (a_1) R (p_t, t_k) t_{May^*, w}^*)_{0 \leq i \leq k}$.

- **Item 2,** $\Leftarrow$: $w \not\in L_f(p)$, and thus $p_0 (a_1) p_1 (a_2) \cdots (a_l) p_t (\cdots) \not\models \tau$. Then, $L_f(p) = \emptyset$. So it must be that $w \not\in L_f(p)$.
trace(proj_p(c)). If |w| = ω, then w ∈ Lω(p) and thus w ∈ Lω(q) (since Lω(p) ⊆ Lω(q)). It follows that we can construct a successful computation c′ ∈ C(q,t) such that w = trace(proj_q(c′)) = trace(proj_p(c′)) = proj_p(c). It follows that q may⊥ t and therefore p ⊑ω_q q. If |w| < ω, we can split the proof into two cases: either w ∈ Lf(p) or w ∈ Lω(q). We can then establish that q may⊥ t as above.

On to Item 2 now. For the ⇒ direction we have that p ⊑ωmust q, w ∈ (A×L)∗ ∪ (A×L)ω such that p w. Then p must⊥ t w * or p must⊥ t w (Lemma 3), then q must⊥ t w * or q must⊥ Lw (Definition 6), thus q w (Lemma 3). We further distinguish two cases, depending on whether |w| = ω or not:

If |w| < ω, let q w q′ for some q′, that is, w ∈ Lf(q). Assume that there is no p′ such that p w p′ and I_p(p′) ⊆ I_q(q′). Suppose that p w p′, that is, w ⊑ Lf(p). Then p must⊥ t w must (Definition 6). However, q must⊥ Lw must does not hold (contrapositive of Lemma 3), a contradiction. Suppose now two cases, depending on whether p can engage in its timed divergent computation and then we force p to not contribute to c′. Thus q w and it is not the case that p must⊥ t.

The proof of Theorem 2 also relies on the properties of the timed tests introduced in Lemma 3.

Proof Theorem 2: For the ⇒ direction, assume that p ⊑ωmust q and let w ∈ (A×L)∗ ∪ (A×L)ω. Then, for Relation (1) w ∈ Lf(q) implies q w w, so it is not the case that q must⊥ t w (by Lemma 3)). Therefore it is not the case that p must⊥ Lw (since p ⊑ωmust q), so p w w, or w ∈ Lf(p), as desired.

For Relation (2) w ∈ Lf(q) \ Lf(q) implies q w w and thus p w w (same as Relation 1) but using Lemma 3(4). In addition, it is not the case that q must⊥ Lw (Lemma 6) and thus p must⊥ Lw must does not hold (since p ⊑ωmust q). Therefore, w ∈ Lf(p), again as desired.

The proofs of Relations (3) and (4) are the same as the proof of Relation (2) using Lemma 3(7) and Lemma 3(8), respectively.

On to the ≤ direction now. We assume that Relations (1), (2), (3), and (4) hold. We further assume that there exists a timed test t such that q must⊥ t does not hold (if such a test does not exist then p ⊑ωmust q for any process p). Thus there exists an unsuccessful computation c = ((q_−1,t_−1),(a_i,d_i),(q_i,t_i))_0≤i≤k ∈ C(q,t), with w = trace(proj_p(c)) = trace(proj_q(c)).

If p w then construct an unsuccessful, infinite computation c′ which resembles c until p can engage in its divergent computation, at which point t can be forced to stop contributing to c′. Thus q w and it is not the case that p must⊥ t.

If p w, |c| < ω, and t_k ⊏ Succ we distinguish two cases:

1) Let w ∈ Lf(q) \ Lm(q). Then there exists some (a_i, d_i) ∈ A × L such that q_k (a_i, d_i) but p_k ⊏ p(t). That is, w, (a_i) ∈ Lm(q) and so (by Relation 3) w, (a_i) ∈ Lm(p). Since p is purely nondeterministic, we can construct a finite computation c′ = ((q_−1,t_−1),(a_i,d_i),(q_i,t_i))_0≤i≤k ∈ C(q,t) where proj_p(c) = proj_q(c), t_i = t_k, and p_i (a_i, d_i). The computation is maximal (since t_k = t_k′ (mod)) and unsuccessful (since |c| < ω and t_k′ ⊏ Succ). Therefore, p must⊥ t does not hold.

2) Let now w ∈ Lm(q) (and thus w ∈ Lm(p)). We can then construct a maximal computation c′ as above and then p must⊥ t does not hold given that q must⊥ t does not hold.

Finally, if p w and |c| = ω, then proj_p(c) ∈ Lm(t), proj_q(c) ∈ Lm(q), and w ∈ Lm(p), we can construct an infinite computation c′ ∈ C(q,t) such that proj_p(c) = proj_q(c′). Similar to the above, c′ is unsuccessful and so p must⊥ t does not hold.

All the cases lead to p ⊑ωmust q, as desired.
V. Timed Test Generation

We now establish an algorithm that generates equivalent timed tests starting from any TPTL formula. This can also be regarded as a relation between TPTL and timed must testing. Our result builds on timed Büchi automata [6] approaches to LTL model checking [4, 5, 16]–[18].

Theorem 4: Given a TPTL formula φ there exists a test Tφ such that p ⊨γ φ for any suitable γ if and only if p mustπ Tφ for any timed process p. Tφ can be algorithmically constructed starting from φ.

Proof: Let p be an arbitrary timed process. Tφ is first constructed to consider only infinite computations, and will then be modified to consider maximal traces. A sub-formula of φ is defined inductively:

1) φ is a sub-formula of φ,
2) any formula t formed by terms of form θ and relational and boolean operators (henceforth called “time formula”) occurring in φ is a sub-formula of φ, but no sub-formula of t is a sub-formula of φ (a time formula is indivisibly a sub-formula),
3) if ¬ξ is a sub-formula of φ, then so is ξ,
4) if O ξ is a sub-formula then so is O ξ, O ∈ \{X, x, \}
5) if ξ O ξ2 is a sub-formula of φ then so are ξ1 and ξ2, O ∈ \{∧, V, U\}.

Let Cφ be the set of exactly all the clocks that occur in a TPTL formula φ, and let C(φ) be the closure of φ, that is, the set of exactly all the sub-formulae of φ. Furthermore, let Θ(φ) ⊆ C(φ) contain exactly all the sub-formulae of φ that are time formulae.

The construction of Tφ is then based on the untimed construction developed by Vardi and Wolper [17]. We first consider the “local” automaton Lφ = (2C(φ), Cφ, Nφ, →L, \{0, Nφ, s0\}). The set of states contains all the subsets of C(φ) that have no internal inconsistency, plus one designated initial state. The local automaton does not impose any acceptance condition. A state s has no internal inconsistency if and only if:

1) ψ ∈ s if and only if ¬ψ ∉ s for all ψ ∈ C(φ),
2) ξ ∧ ψ ∈ s if and only if ξ ∈ s, ψ ∈ s for all ξ ∧ ψ ∈ C(φ),
3) x.ψ ∈ s implies ψ ∈ s.

The transition relation is defined as s →L t if and only if a = t, C = \{x ∈ Cφ : x.ψ ∈ s ∧ ψ ∈ t\}, t = \{θ(φ) ∩ s, ψ ∈ t ∧ x.ψ ∈ s implies x.ψ ∈ t, and
1) s = s0 and φ ∈ a, or
2) s ≠ s0, for all ψ ∈ C(φ), X.ψ ∈ s if and only if ψ ∈ t, and for all ξ U φ ∈ C(φ) either ψ ∈ s, or ξ ∈ s and ξ U ψ ∈ t.

Lφ does not impose any acceptance conditions as mentioned, but enforces all the time constraints present in the original formula φ. Indeed, at every moment frozen in time by a construction x.ψ we reset the respective clock in the local automaton (for the set C of clocks reset by a transition out of s contains exactly all the sets of clocks x reset by an x. construction in s). Later, whenever a time formula is encountered, that formula is added to the time constraints that enable the transition. Checking the time formula to determine that the transition is enabled has the intended effect: the time formula needs to be true for the whole formula φ to be true, and the semantics of time in a timed transition system ensures that every clock measures the time from when it was reset in the transition system (that is, frozen in the formula) to the current time.

Acceptance is handled by the “eventuality” automaton Eφ = (2L(φ), \{∅, 2L(φ) \}, →E, \{∅\}, \{∅\}), with E(φ) = \{ξ U ψ ∈ C(φ)\} and s →E t if and only if
1) s = ∅ and ξ U ψ ∈ t if and only if ξ U ψ ∈ a for all ξ U ψ ∈ a,
2) s ≠ ∅ and ξ U ψ ∈ t if and only if ξ U ψ ∈ s.

The eventuality automaton is identical to the one developed elsewhere [17]. It tries to satisfy the eventualities of the formula (with no regard for time constraints). The current state keeps track of which eventualities have yet to be satisfied.

The test Tφ is obtained by taking the cross-product of Lφ and Eφ. The cross-product is taken using the usual (untimed) construction [17], for only the transitions in →L contain time constraints and/or clock resets (and these go into the composite automaton together with the actions that accompany them in Lφ). This test characterizes traces over 2L(φ) × L; in order to switch to A × L we project over A the action labels of all the transitions, as done previously [17].

The construction of Tφ follows carefully the construction for the untimed case. It is then immediate that Tφ is correct as far as untimed words are concerned, in the sense that trace(projφ(e)) ⊨ φ for exactly all the successful infinite computations e ∈ C(p, Tφ) stripped of time information. The timing information is added (via Lφ), as detailed above. In all, we have:

trace(projφ(e)) ⊨γ φ for exactly all the successful infinite computations e ∈ C(p, Tφ) (5)

We now enhance Tφ so that it also accepts finite maximal traces: For every state s in Tφ, we check if all the formulæ contained in s are satisfied by the trace e. Checking for acceptance of the trace e (like for any fixed trace) can be done algorithmically along the structure of the formula φ. Then, for every state s in Tφ such that each TPTL formula φ labeling s is satisfied by e, we add a transition s → Δ, where Δ is a new state. We use Δ to distinguish from other states also having no outgoing transitions; these states represent deadlock due to inconsistent sub-formulae of φ. The final states of Tφ will then be the set containing only the state Δ thus introduced. We have:

trace(projφ(e)) ⊨γ φ for exactly all the successful maximal computations e ∈ C(p, Tφ) (6)

Indeed, χ(p) ⊨γ φ (with χ(x) = trace(projφ(x))) implies s0 ⇝(Tφ) s → (where s0 is the initial state of Tφ). That is s0 ⇝(Tφ) s ≠ ⇝. Thus, e is maximal. Conversely, if e is successful and maximal, then there exits s0 ⇝(Tφ) s ≠ ⇝ in Tφ. According to the algorithm there exists then s0 ⇝(Tφ) s → Δ. Thus, e is maximal.

That p ⊨γ φ if and only if p mustπ Tφ follows from Properties (5) and (6), as desired.
VI. CONCLUSIONS

We proposed in this paper a model of timed tests based on timed transition systems. We addressed the problem of characterizing infinite behaviours of timed processes by developing a theory of timed $\omega$-final states. This theory is inspired by the acceptance family of Büchi automata. We also extended the testing theory of De Nicola and Hennessy to timed testing. We then studied the derived timed may and must preorders and developed an alternative characterization for them. This characterization is very similar to the characterization of De Nicola and Hennessy’s testing preorders, which shows that our preorders are fully back compatible: they extend the existing preorders as mentioned, but they do not take anything away. Further into the characterization process we also showed that the timed must preorder is equivalent to a variant of reverse timed trace inclusion when its first argument is purely nondeterministic.

We then presented an algorithm for test generation out of TPTL formulae. Both processes and tests are represented by timed transition systems instead of automata (that is, their number of states is not necessarily finite). This is consistent with the huge body of similar constructs in the untimed domain. However, the timed tests produced out of TPTL formulae (Theorem 4) are always finite automata (meaning that their set of states is always finite). This is quite nice to have for a very practical reason, as infinite-state tests must be further refined to become practical characterization tools.

One significance of our results stems from the fact that while algorithms and techniques for real-time testing have been studied actively [11], [12], the domain still lacks solid techniques and theories. Our paper attempts to present a general theoretical framework for real-time testing, in order to facilitate the subsequent evolution of the area. To serve such a purpose our framework is as close as possible to the original framework of (untimed) testing, as shown in our characterization theorems. In addition, our characterization is surprisingly concise in terms of the test cases needed.

Beside the obvious use of the algorithm for test generation, the algorithm also relates the satisfaction relation of TPTL to the must$_\oplus$ operator. We therefore note that the algebraic and logic specification techniques attempt to achieve the same thing (conformance testing) in two different ways. Each of them is more convenient for certain systems, as they both have advantages and disadvantages. Therefore our test generation algorithm also forms the basis of bringing logic and algebraic specifications together, thus obtaining heterogeneous specifications for real-time systems that combine the advantages of the two paradigms. This has the potential of providing a uniform basis for analyzing heterogeneous real-time system specifications with a mixture of timed transition systems and timed logic formulae.

We consider TPTL without congruence because the theory of timed transition systems does not offer a congruence mechanism. Such a mechanism could likely be introduced without much difficulty, but to our knowledge none of the temporal logics used in practical settings take congruence into consideration, so we preferred to leave time transition systems intact and exclude congruence from TPTL instead. This does diminish the expressiveness of TPTL [7], but we are still significantly above the expressiveness of most real-time temporal logics [7], [10].

We avoid the discussion of discrete versus continuous time. All the results and definitions are oblivious to whether time is considered discrete or continuous. We therefore leave the decision of discreteness to the future uses of this work.

This paper is only a first step in the direction of combining operational and assertional styles of timed specifications; the studying of techniques mixing operators from timed process algebras and TPTL is a widely open area. Indeed, we established an algorithm for constructing timed tests from TPTL formulae, but how to go the other way around is still open for research. The timed preorder testing developed from De Nicola and Hennessy’s preorder testing is not the only testing relation; other testing relations with the addition of time constraints will also be exciting to investigate.

ACKNOWLEDGMENT

This research was supported by the Natural Sciences and Engineering Research Council of Canada. Part of this research was additionally supported by Bishop’s University.

REFERENCES


105